

1-st stage

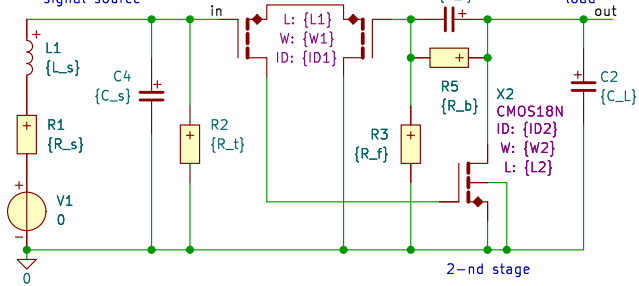
Receive coil
signal source

X1
CMOS18PD

C3
{C_i}

ADC
load

out



2-nd stage

A1

A2

A3

.source V1

.detector V_out

.lgref Gm_M1_X2