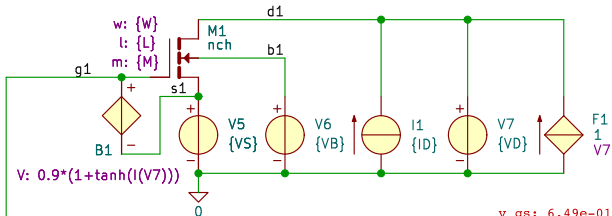
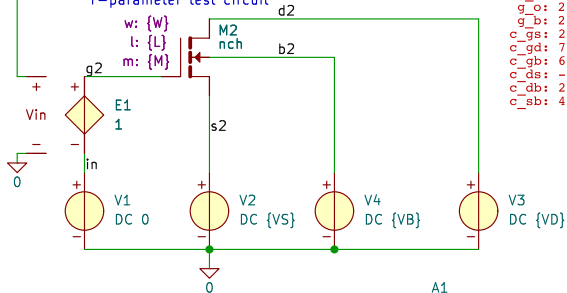


Biasing circuit determines VGS  
at VS, VB, VD, and ID setpoint



$v_{gs}: 6.49e-01$   
 $v_{ds}: 9.00e-01$   
 $i_{ds}: 1.00e-05$   
 $g_m: 8.48e-05$   
 $g_o: 2.57e-06$   
 $g_b: 2.30e-05$   
 $c_{gs}: 2.28e-16$   
 $c_{gd}: 7.34e-17$   
 $c_{gb}: 6.48e-17$   
 $c_{ds}: -2.02e-19$   
 $c_{db}: 2.94e-16$   
 $c_{sb}: 4.18e-16$

Y-parameter test circuit



A1

.lib ../lib/log018.l TT