# Application description



## **Design of amplifier configurations**



Table design specification			
ain of receiver OpAmp.	min 6555.0	typ	max units
e of the receiver transmission coefficient $A$	$4.046\cdot10^{-7}$		S
e receiver transmission coefficient $A$ (integrating receiver)	$4.046\cdot10^{-7}$		s
e of the transmitter transmission coefficient $B$		27.07	Ω
roduct of receiver OpAmp.	$9.425\cdot 10^4$		$_{\rm Hz}$
roduct of transmitter OpAmp.	$1.64\cdot 10^4$		$_{\rm Hz}$
rent drive capability	0.000125		A
drive current of the transmit coil	0.03694		A
ping resistance		$4.443\cdot 10^4$	Ω
damping resistance		1200.0	Ω
value for noise contribution of I_noise_R2.		$6.464 \cdot 10^{8}$	Ω
value for noise contribution of I_noise_R1.		235.0	$\Omega_{\rm V}$
tage slew rate		7854.0	v s
voltage slew rate	$3.332\cdot 10^4$		<u>V</u> s
value for noise contribution of I1_XU1.		$\frac{9.703 \cdot 10^{-24}}{2.492 \cdot 10^{-6} R_i^2 + 0.001197 R_i + 1}$	$\frac{A^2}{Hz}$
value for noise contribution of I1.		$rac{1.071\cdot 10^{-11}}{R_g^2}$	$\frac{A^2}{Hz}$
value for noise contribution of V1_XU1.		$3.893\cdot10^{-18}$	$\frac{V^2}{Hz}$
value for noise contribution of V2.		$1.071 \cdot 10^{-11}$	$\frac{V^2}{Hz}$
peak voltage drive capability	0.3536		V
tter voltage	1.061		V
r input voltage	0.004494		V

	AALOO		,,		.,	por acional / cilipin
1	Feature	es				
•	Low Volta	age Nois	se: 2.2 nV/\	Hz at 1 k	κHz	
•	0.1-Hz to	10-Hz N	Noise: 130	nV <sub>PP</sub>		
•	Low Quie	escent C	urrent: 2.5	mA/Ch (I	Maximum)	
•	Low Offs	et Voltaç	ge: 150 μV	(Maximu	m)	
•	Gain Bar	ndwidth F	Product: 18	MHz		
•	Slew Rat	e: 6.4 V	/µs			
•	Wide Sup	oply Ran	ige:	e V		
	±2.25 V t Bail-to-P	u ±iov, ail Outru	, 4.5 V 10 3 it	0 V		
•	Short-Cir	cuit Cur	n rent: +65 m	Δ		
•	Available	in 5-Pin	SOT-23 8	Pin MS	ЭР	
	/ / / / / / / / / / / / / / / /					
	8-Pin SO	IC, and	14-Pin TSS	SOP Pack	kages	
	8-Pin SO	IC, and	Asympto	OP Pack	nodel receiv	ver — asymptotic
10	8-Pin SO	IC, and	Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain servo
10	4	IC, and	Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —servo —direct —gain
10	8-Pin SO	IC, and	Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —servo —direct —gain
10 10	8-Pin SO	IC, and	Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —servo —direct —gain
10 10 10 <sup>-</sup>	8-Pin SO	IC, and	Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —servo —direct —gain
10 10	8-Pin SO		Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —servo —direct —gain
10 10 10 <sup></sup>	8-Pin SO		Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —servo —direct —gain
10 10 10 <sup></sup>	8-Pin SO		Asympto	tic-gain r	nodel receiv	ver —asymptotic —loopgain —direct —gain
10 10 10 <sup></sup> 10 <sup></sup>	8-Pin SO		Asympto	tic-gain r	nodel receiv	ver —asymptotic —loopgain —direct —gain
10 10 10 <sup></sup> 10 <sup></sup>	8-Pin SO		Asympto	SOP Pack	nodel receiv	ver —asymptotic —loopgain —gain —gain