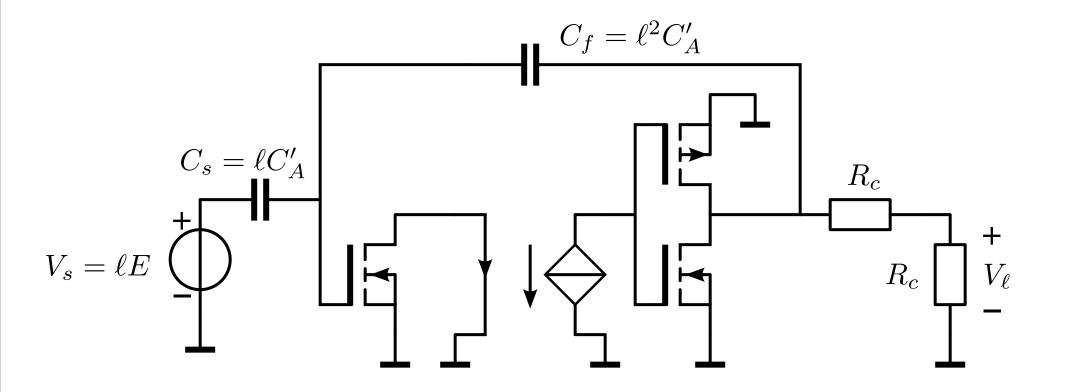
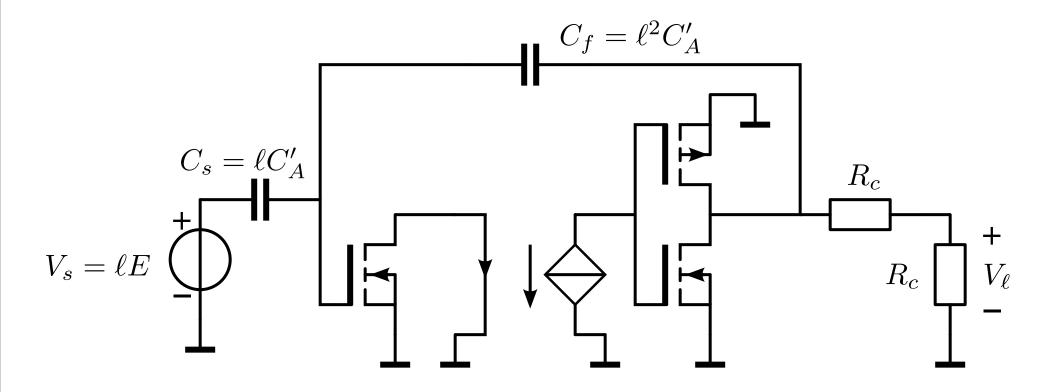
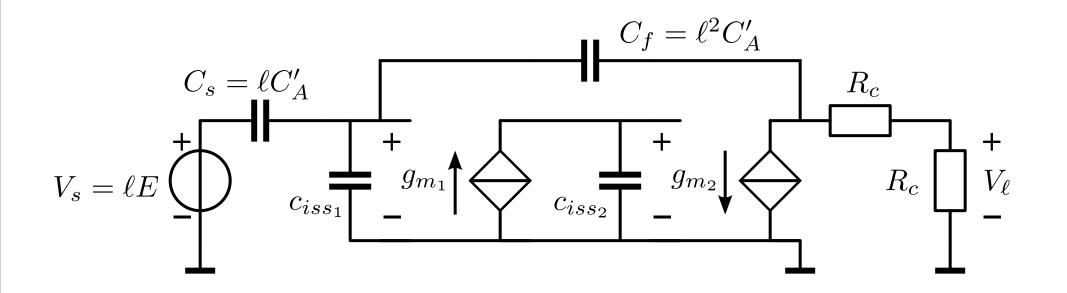
# **Structured Electronic Design**

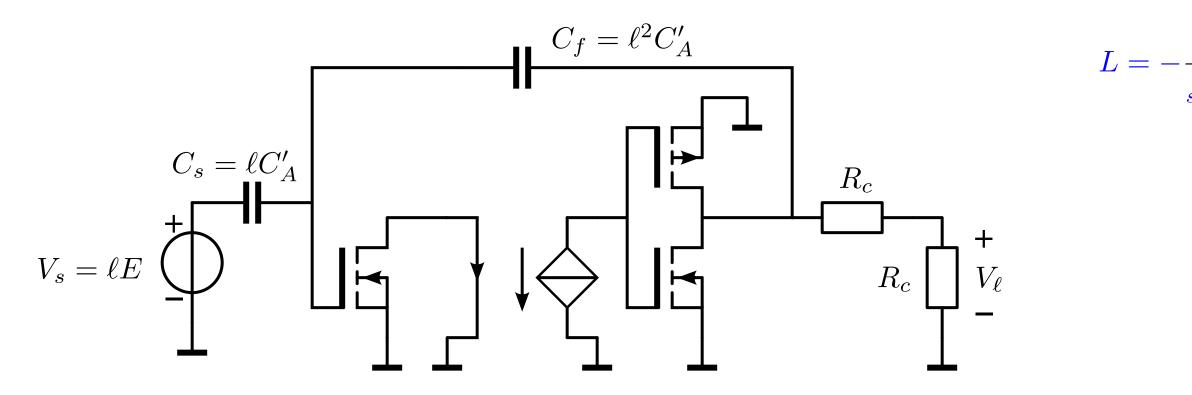
EE4109 Design of the small-signal dynamic behavior of the active antenna

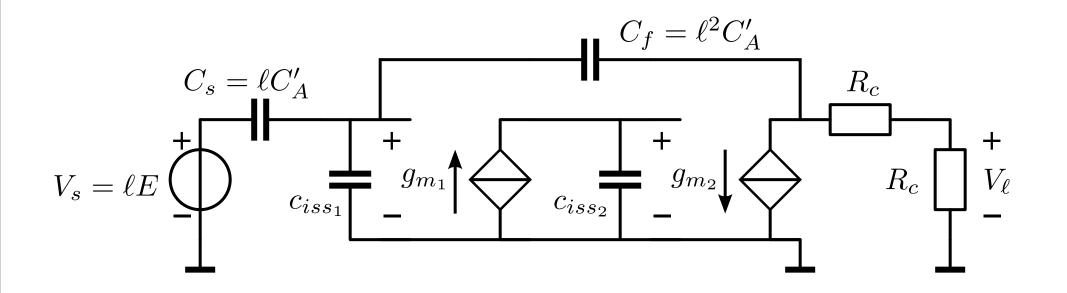
Anton J.M. Montagne



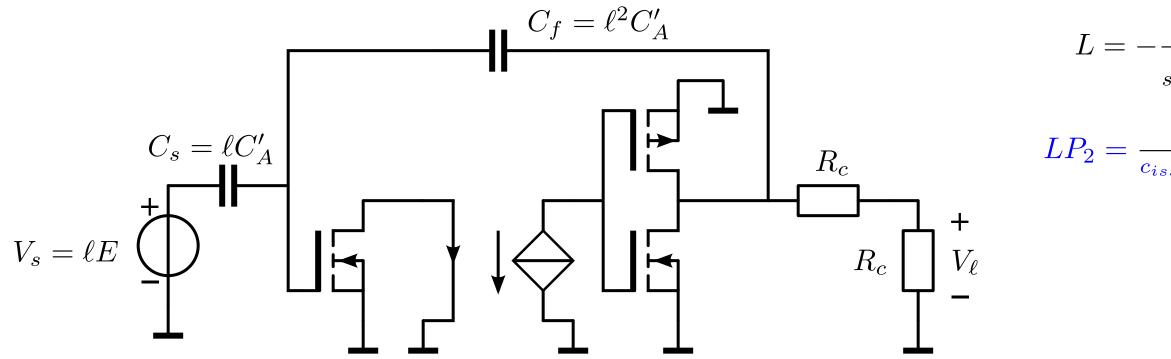


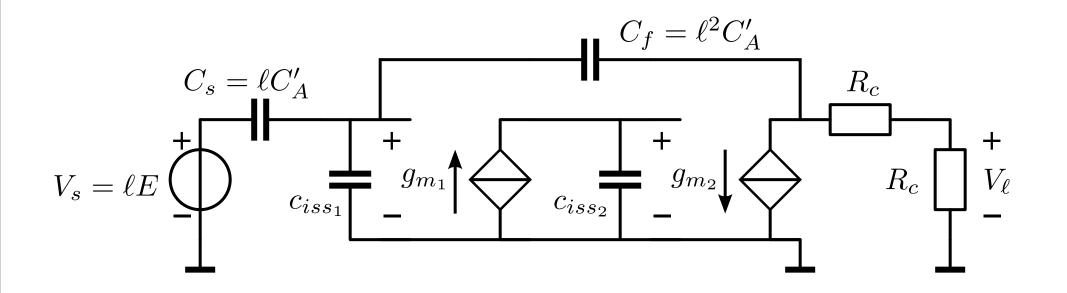




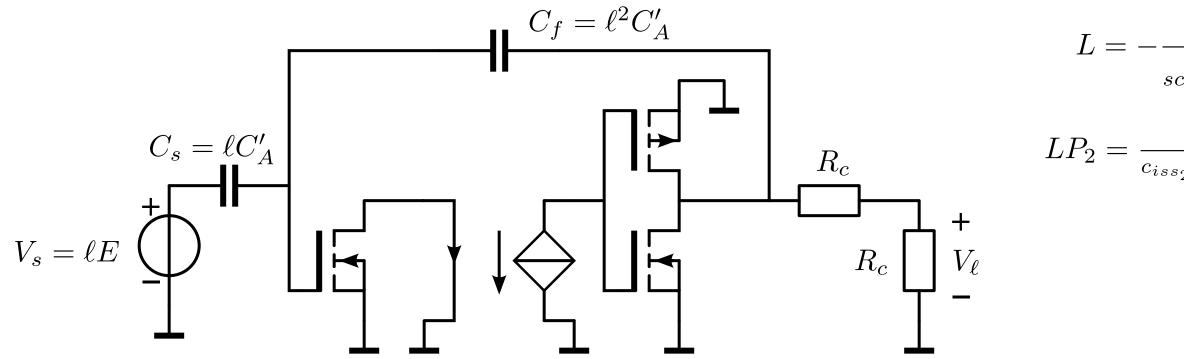


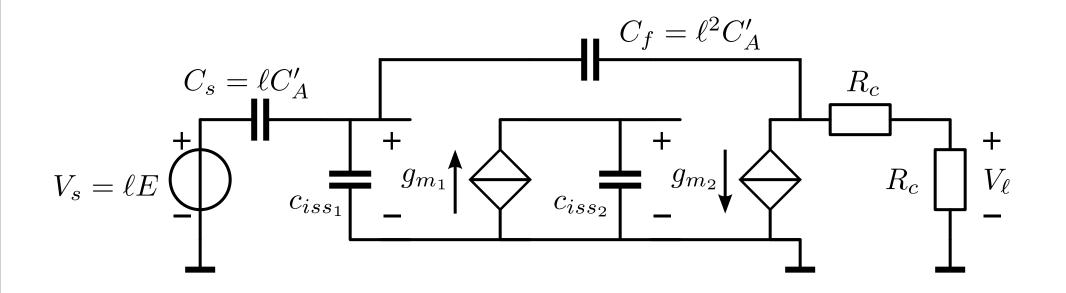
 $L = -\frac{2g_{m_1}g_{m_2}R_c \frac{C_f}{C_f + C_s + c_{iss_1}}}{sc_{iss_2} \left(1 + s2R_c \frac{C_f \left(C_s + c_{iss_1}\right)}{C_f + C_s + c_{iss_1}}\right)}$ 





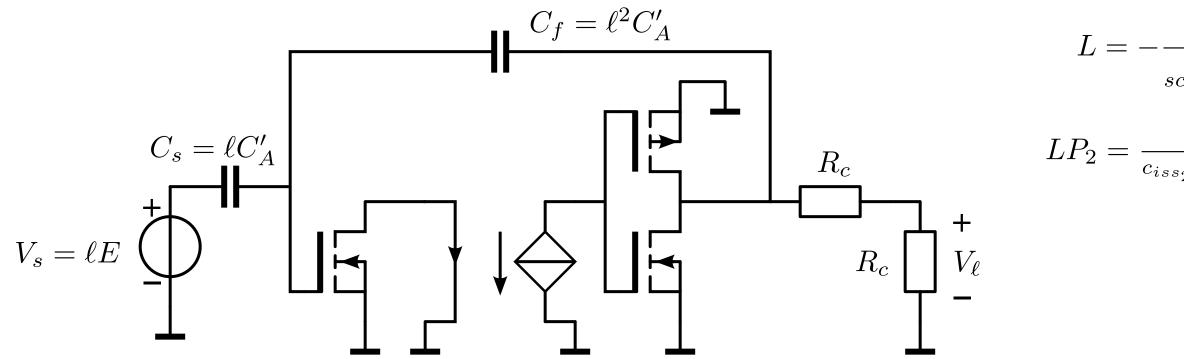
$$\frac{2g_{m_1}g_{m_2}R_c \frac{C_f}{C_f + C_s + c_{iss_1}}}{sc_{iss_2} \left(1 + s2R_c \frac{C_f \left(C_s + c_{iss_1}\right)}{C_f + C_s + c_{iss_1}}\right)}$$
$$\frac{g_{m_1}g_{m_2}}{s_2 \left(C_s + c_{iss_1}\right)}$$

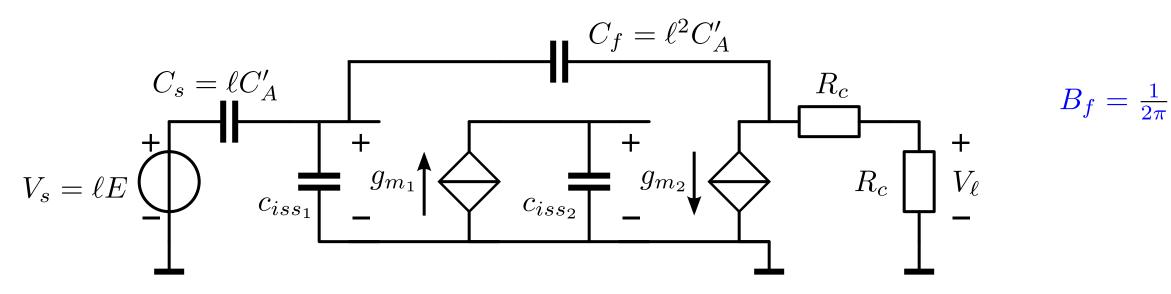




$$\frac{2g_{m_1}g_{m_2}R_c \frac{C_f}{C_f + C_s + c_{iss_1}}}{sc_{iss_2} \left(1 + s2R_c \frac{C_f \left(C_s + c_{iss_1}\right)}{C_f + C_s + c_{iss_1}}\right)}$$
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 $g_{m_1} = 23 \mathrm{m}$  $g_{m_2} = 28 \mathrm{m}$  $C_s = 5 \mathrm{p}$  $c_{iss_1} = 1 \mathrm{p}$  $c_{iss_2} = 1.2 \mathrm{p}$ 

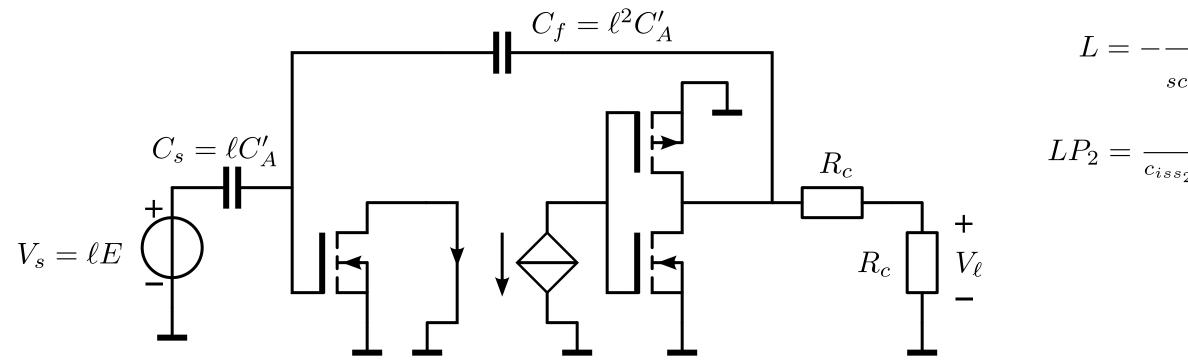


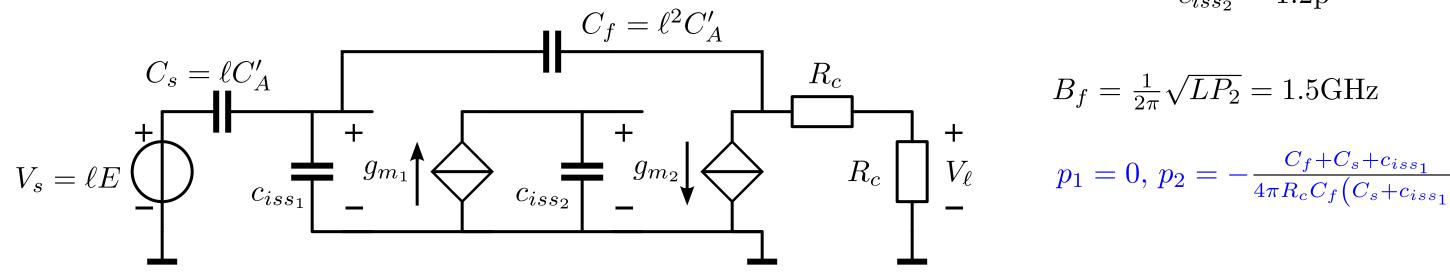


$$\frac{2g_{m_1}g_{m_2}R_c \frac{C_f}{C_f + C_s + c_{iss_1}}}{sc_{iss_2} \left(1 + s2R_c \frac{C_f \left(C_s + c_{iss_1}\right)}{C_f + C_s + c_{iss_1}}\right)}$$
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$$g_{m_1} = 23m$$
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$$c_{iss_1} = 1p$$
$$c_{iss_2} = 1.2p$$

 $B_f = \frac{1}{2\pi}\sqrt{LP_2} = 1.5 \text{GHz}$ 



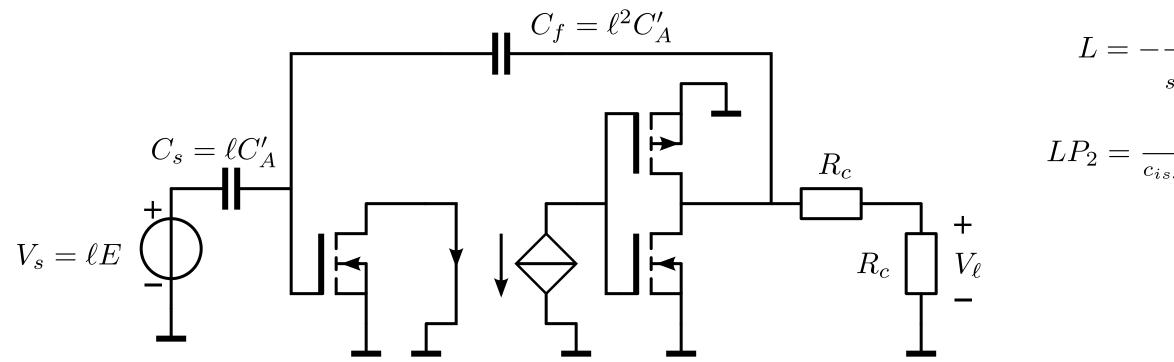


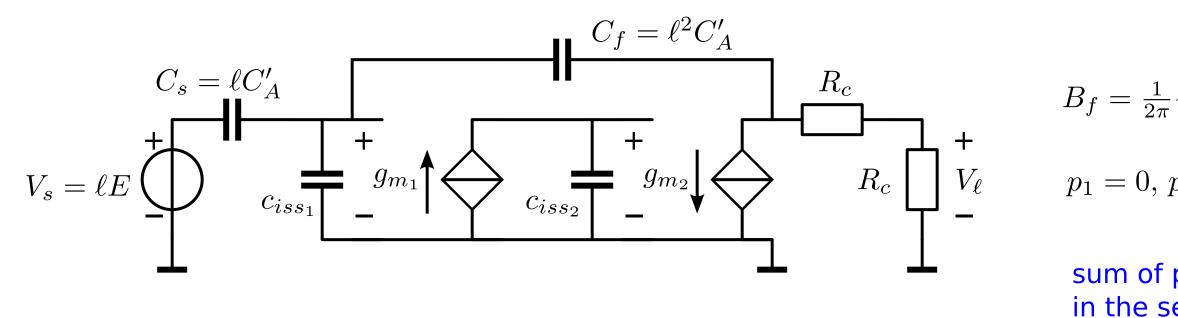
$$\frac{2g_{m_1}g_{m_2}R_c \frac{C_f}{C_f + C_s + c_{iss_1}}}{sc_{iss_2} \left(1 + s2R_c \frac{C_f \left(C_s + c_{iss_1}\right)}{C_f + C_s + c_{iss_1}}\right)}$$

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 $= -450 \mathrm{MHz}$ 





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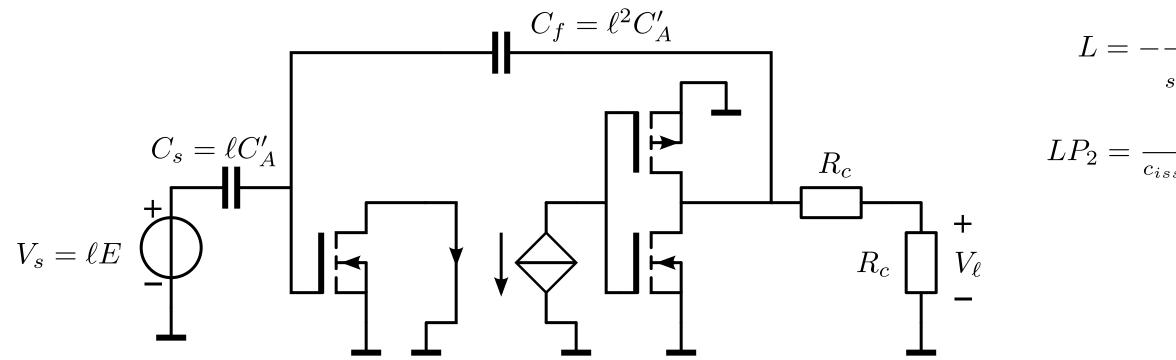
$$\frac{g_{m_1}g_{m_2}}{s_2 \left(C_s + c_{iss_1}\right)}$$

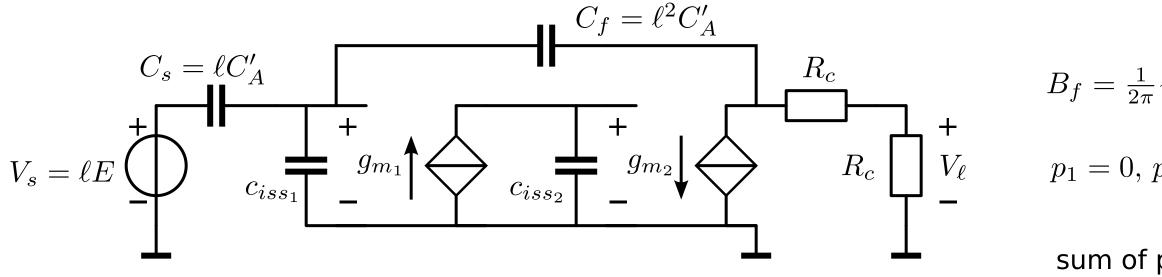
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$$p_2 = -\frac{C_f + C_s + c_{iss_1}}{4\pi R_c C_f (C_s + c_{iss_1})} = -450 \text{MHz}$$

sum of poles (abs) increased as a result of pole-splitting in the second stage (first stage is shorted)





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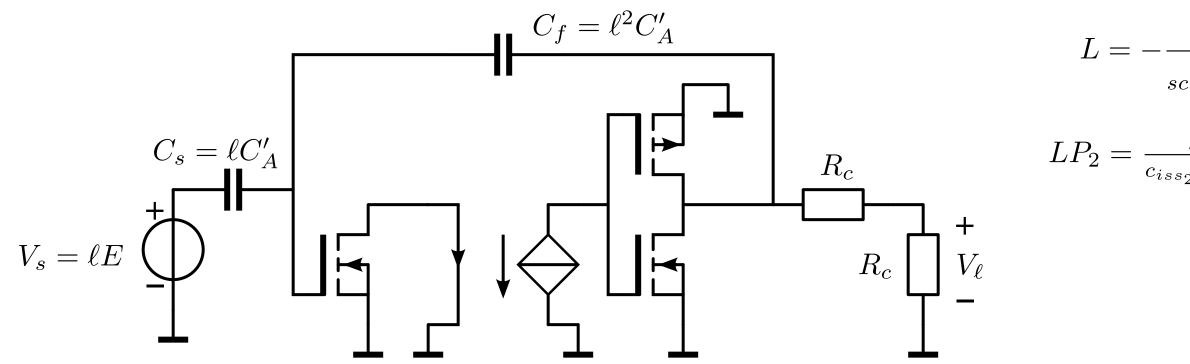
$$\frac{g_{m_1}g_{m_2}}{s_2 \left(C_s + c_{iss_1}\right)}$$

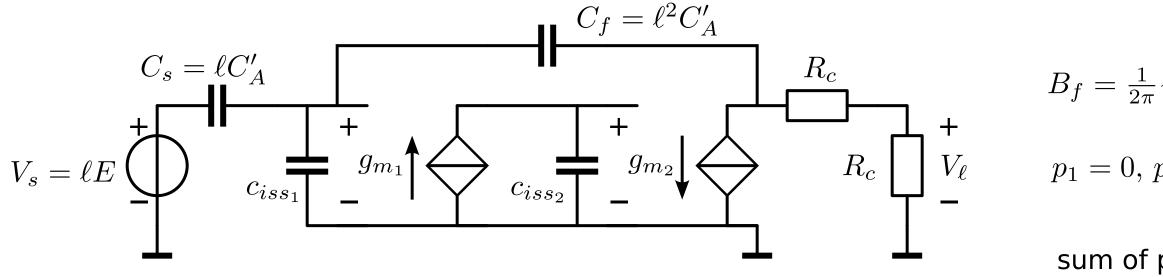
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### DualStage.py





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### DualStage.py

A collection of techniques for correcting the frequency response

# A collection of techniques for correcting the frequency response Obtain the desired filter characteristic

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V/I drive capability

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V/I drive capability Noise Bandwidth Accuracy Power losses

- V/I drive capability
- Noise
- Bandwidth
- Accuracy
- Power losses
- Energy storage

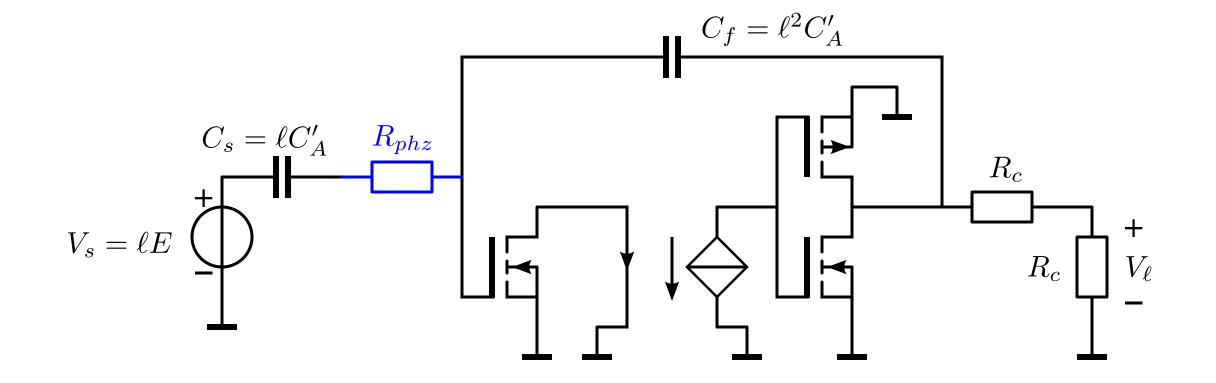
- V/I drive capability
- Noise
- Bandwidth
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- Weak nonlinearity

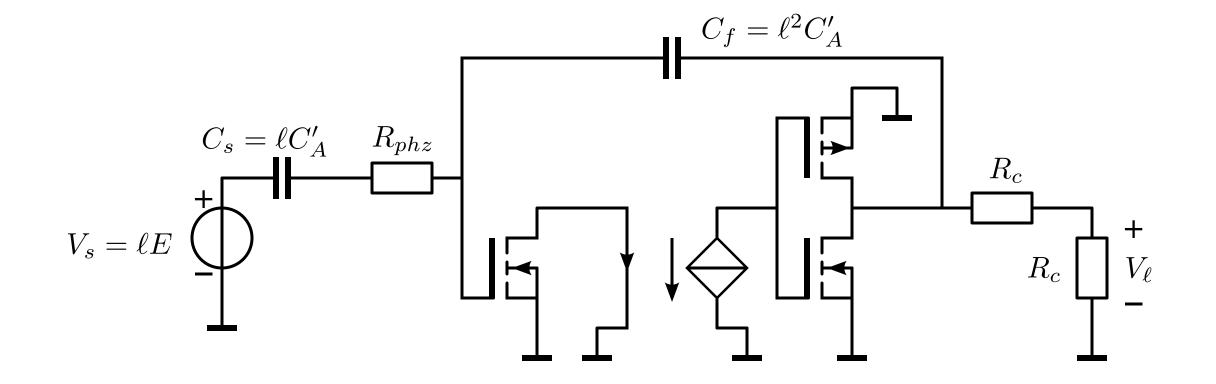
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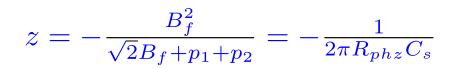
# **Structured Electronic Design**

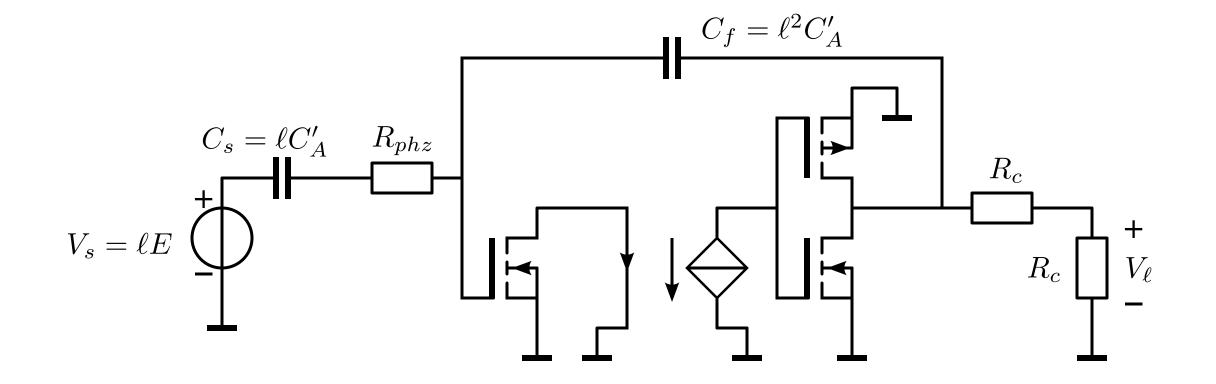
## EE4109 Phantom-zero compensation

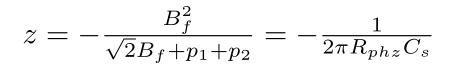
Anton J.M. Montagne



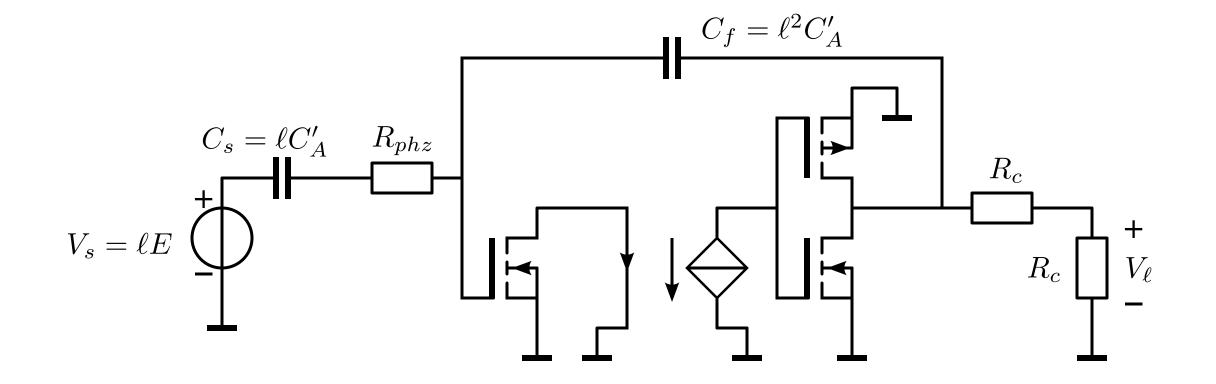


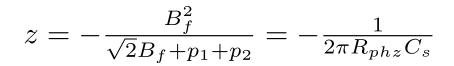






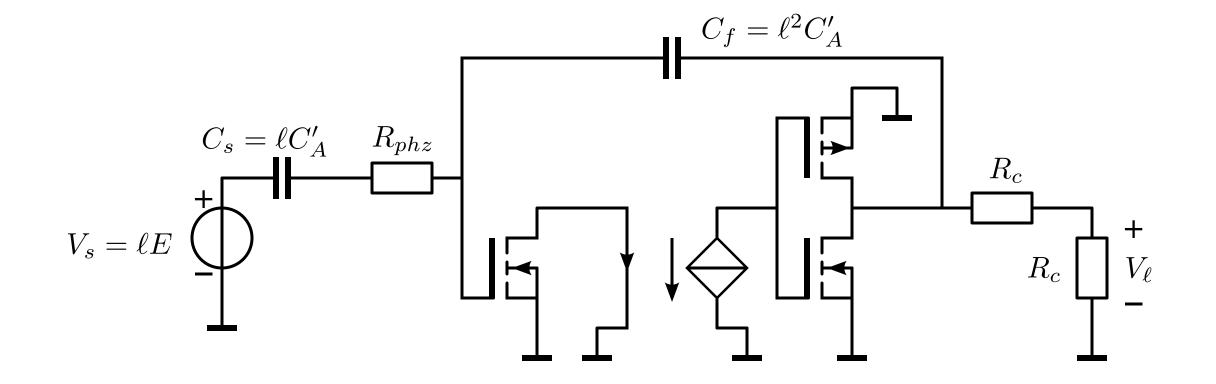
Resistor breaks loop of capacitors:





### Resistor breaks loop of capacitors:

 $C_s, \, c_{iss_1}$ 

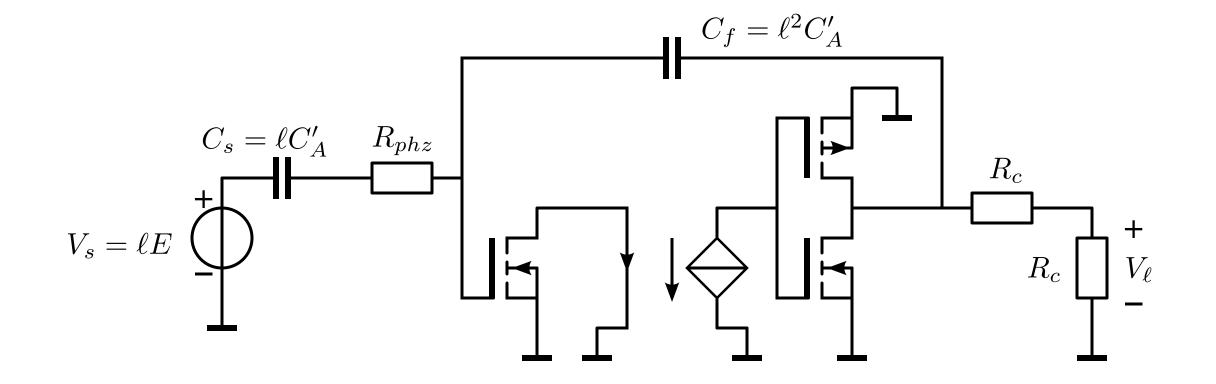


$$z = -\frac{B_f^2}{\sqrt{2}B_f + p_1 + p_2} = -\frac{1}{2\pi R_{phz}C_s}$$

### Resistor breaks loop of capacitors:

 $C_s, c_{iss_1}$ 

### Third non-dominant pole in the loop gain:



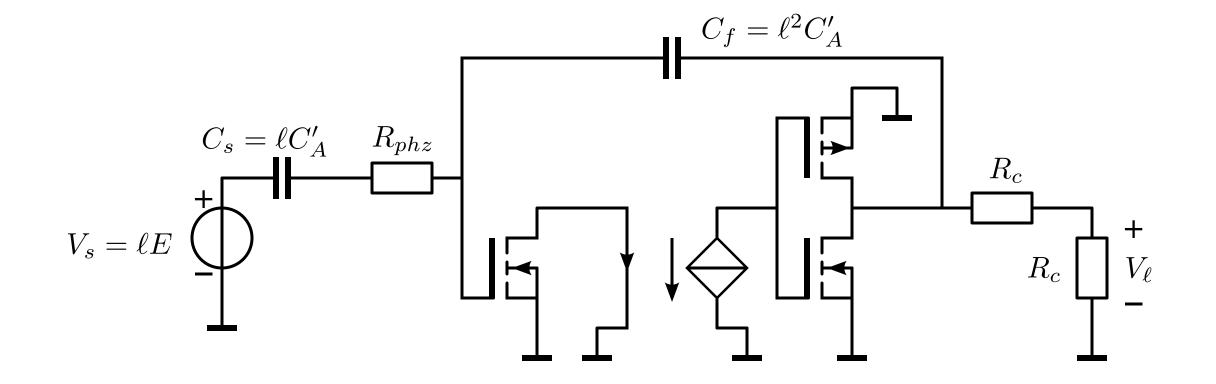
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### Resistor breaks loop of capacitors:

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### Third non-dominant pole in the loop gain:

 $p_1 \approx -\frac{1}{2\pi R_{phz} c_{iss_1}}$ 



$$z = -\frac{B_f^2}{\sqrt{2}B_f + p_1 + p_2} = -\frac{1}{2\pi R_{phz}C_s}$$

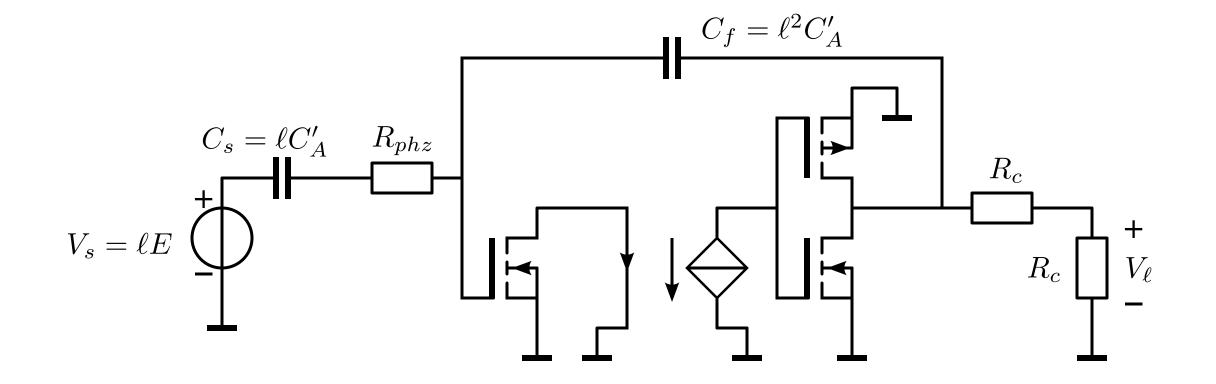
### Resistor breaks loop of capacitors:

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Third non-dominant pole in the loop gain:

$$p_1 \approx -\frac{1}{2\pi R_{phz} c_{iss_1}}$$

### DualStagePhZ1.py



$$z = -\frac{B_f^2}{\sqrt{2}B_f + p_1 + p_2} = -\frac{1}{2\pi R_{phz}C_s}$$

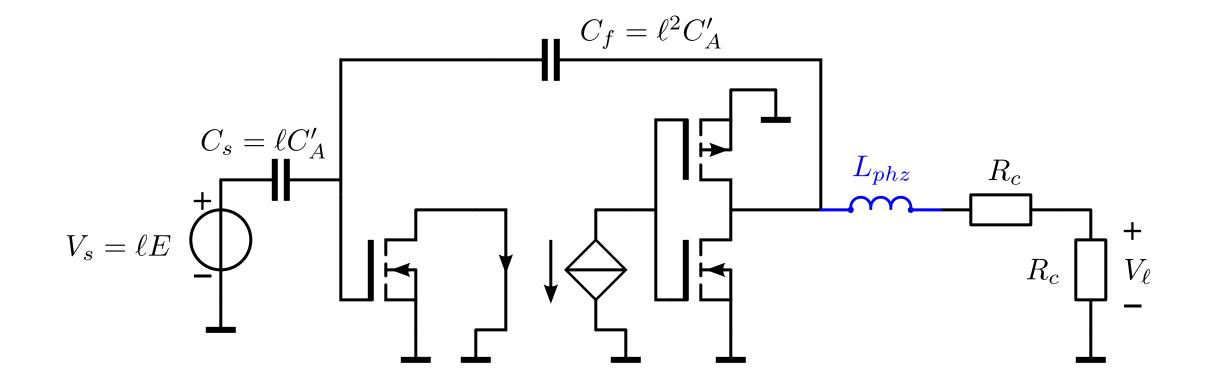
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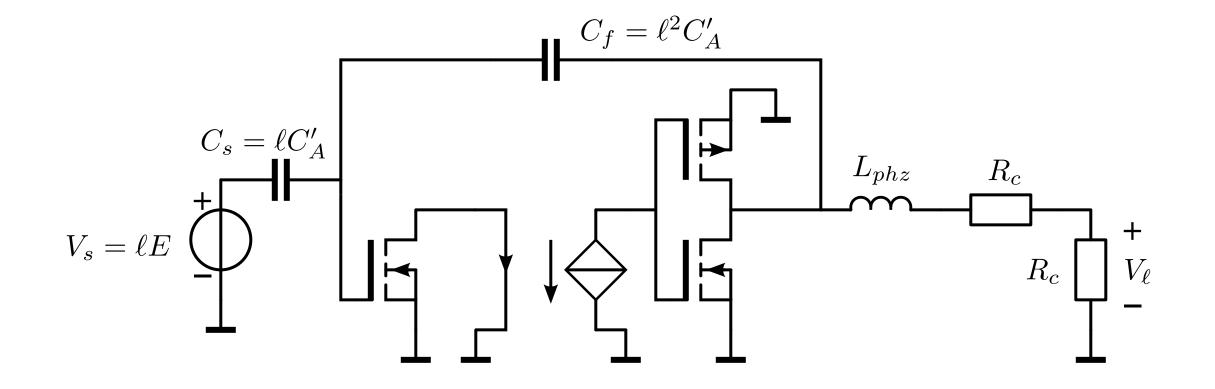
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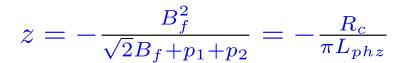
Third non-dominant pole in the loop gain:

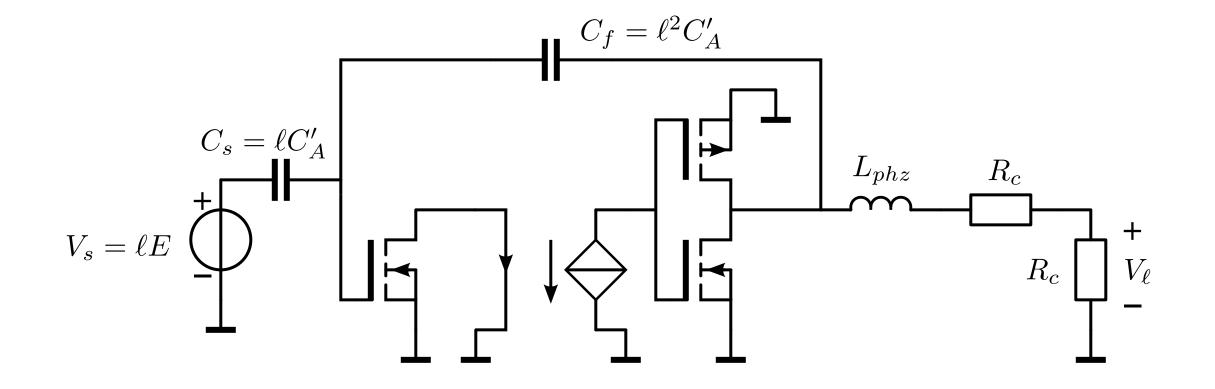
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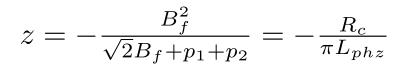
### DualStagePhZ1.py



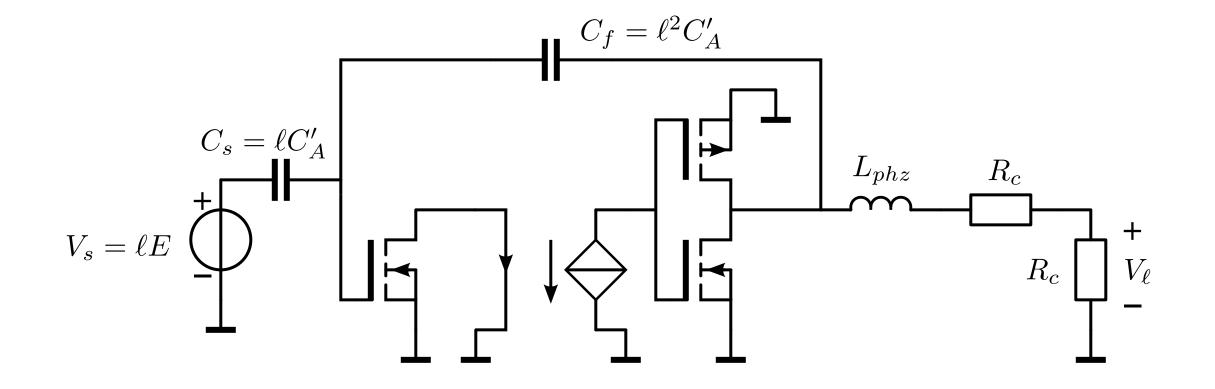








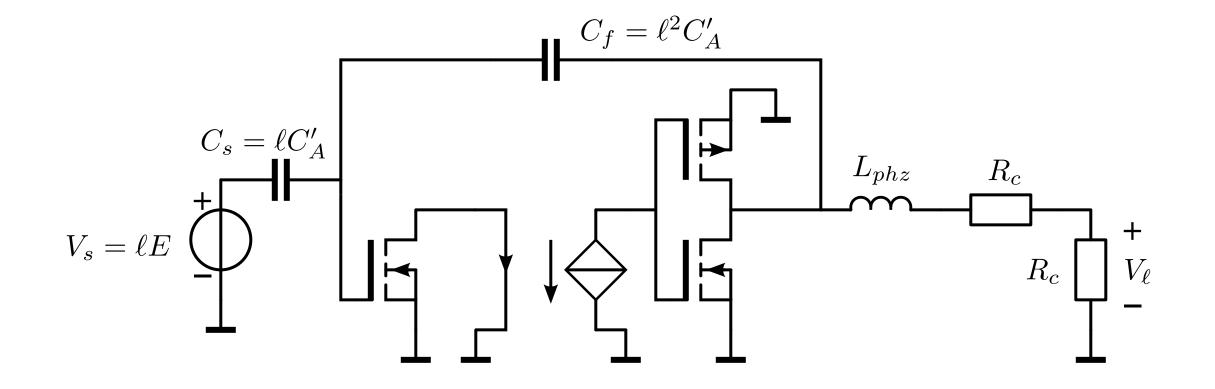
Inductor adds one pole to the loop gain and changes the initial pole positions.



$$z = -\frac{B_f^2}{\sqrt{2}B_f + p_1 + p_2} = -\frac{R_c}{\pi L_{phz}}$$

Inductor adds one pole to the loop gain and changes the initial pole positions.

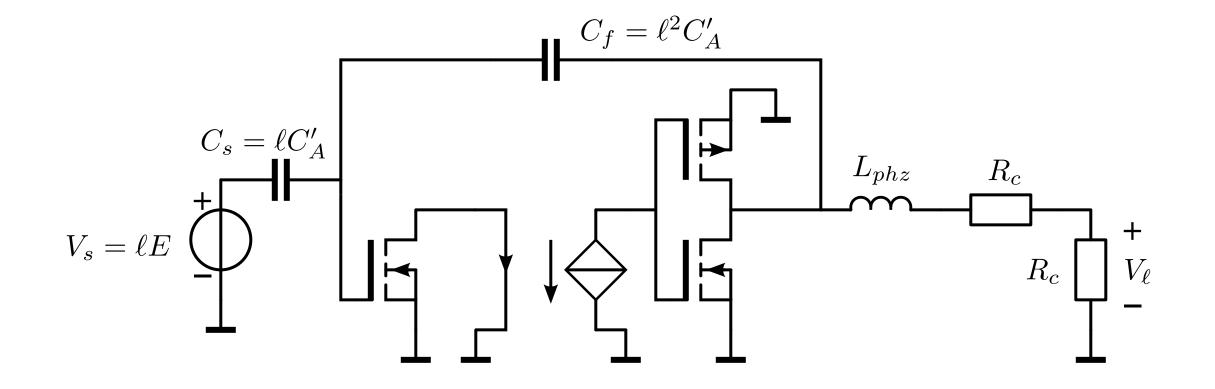
 $p_2 p_3 = \frac{C_f + C_s + c_{iss_1}}{4\pi^2 L_{phz} C_f \left(C_s + c_{iss_1}\right)}$ 



$$z = -\frac{B_f^2}{\sqrt{2}B_f + p_1 + p_2} = -\frac{R_c}{\pi L_{phz}}$$

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$$p_2 p_3 = \frac{C_f + C_s + c_{iss_1}}{4\pi^2 L_{phz} C_f \left(C_s + c_{iss_1}\right)}$$
$$Q = \frac{\pi \sqrt{p_1 p_2} L_{phz}}{R_c}$$



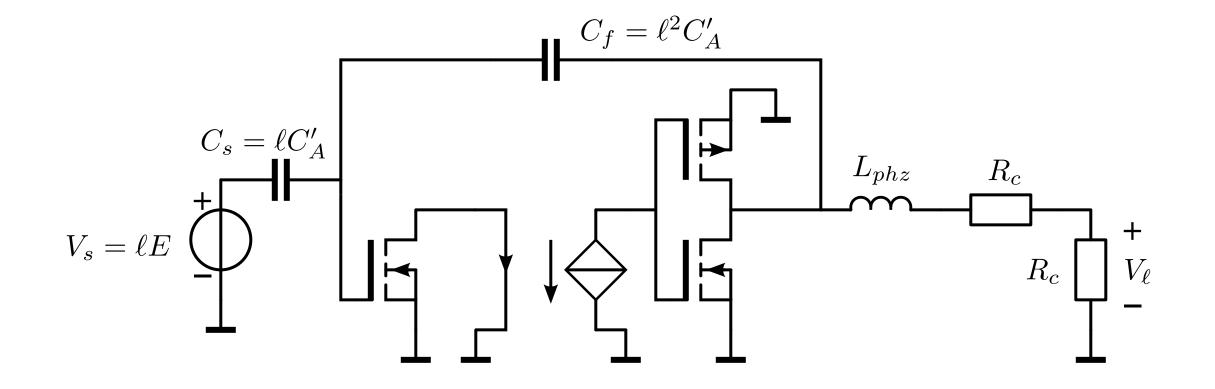
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$$Q = \frac{\pi \sqrt{p_1 p_2} L_{phz}}{B}$$

 $R_c$ 

DualStagePhZ2.py



$$z = -\frac{B_f^2}{\sqrt{2}B_f + p_1 + p_2} = -\frac{R_c}{\pi L_{phz}}$$

Inductor adds one pole to the loop gain and changes the initial pole positions.

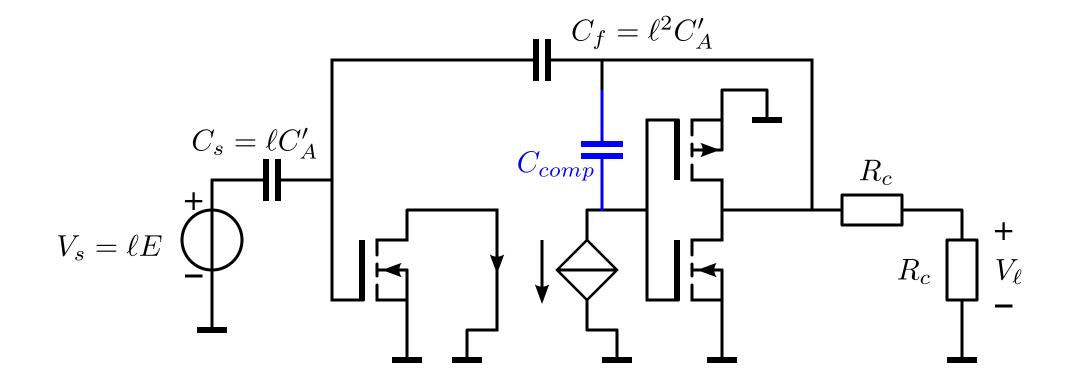
$$p_2 p_3 = \frac{C_f + C_s + c_{iss_1}}{4\pi^2 L_{phz} C_f \left(C_s + c_{iss_1}\right)}$$
$$Q = \frac{\pi \sqrt{p_1 p_2} L_{phz}}{R_c}$$

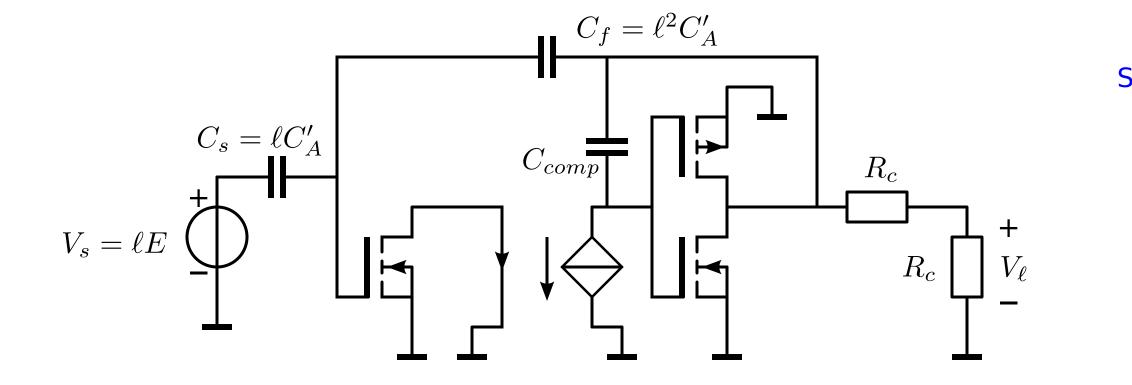
#### DualStagePhZ2.py

#### **Structured Electronic Design**

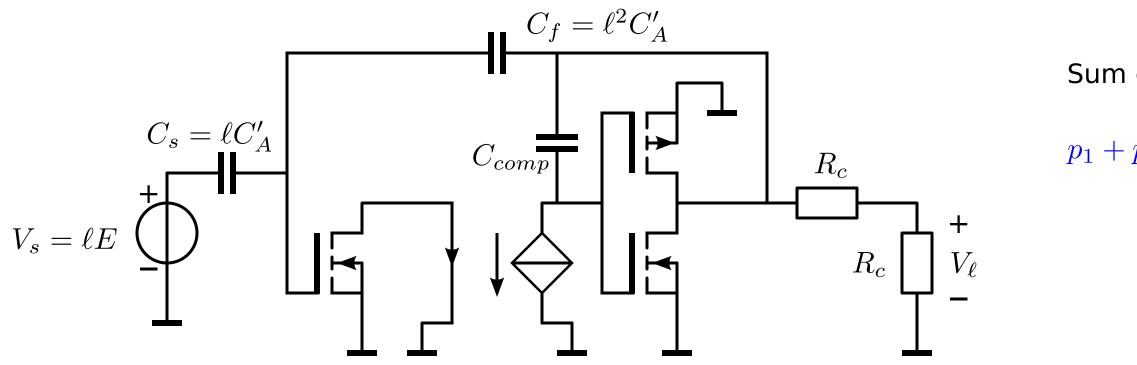
## EE4109 Pole-splitting

Anton J.M. Montagne



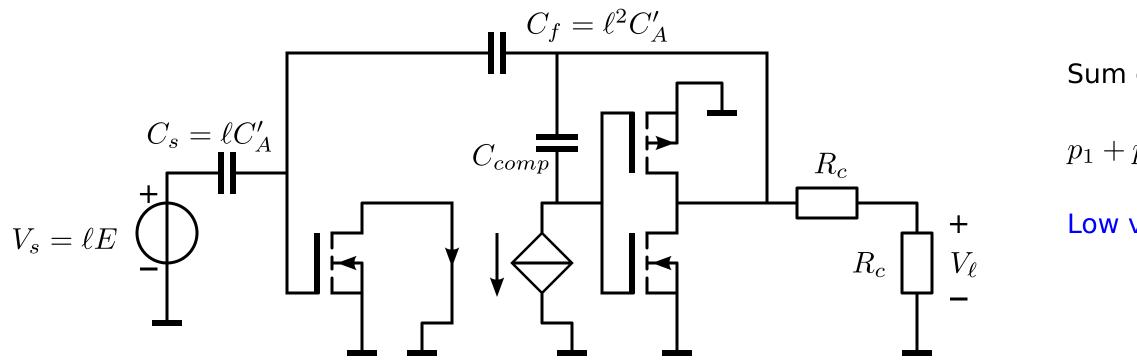


Sum of the poles should be increased:



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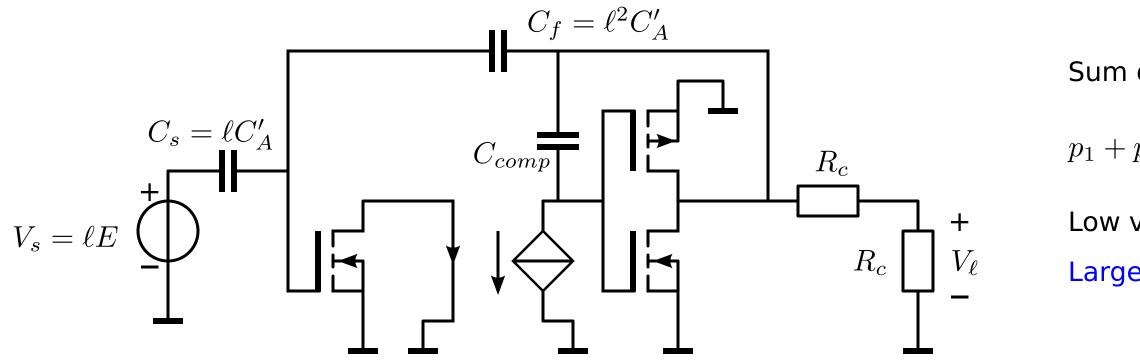
 $p_1 + p_2 = p_2 = -\sqrt{2}B_f$ 



Sum of the poles should be increased:

 $p_1 + p_2 = p_2 = -\sqrt{2}B_f$ 

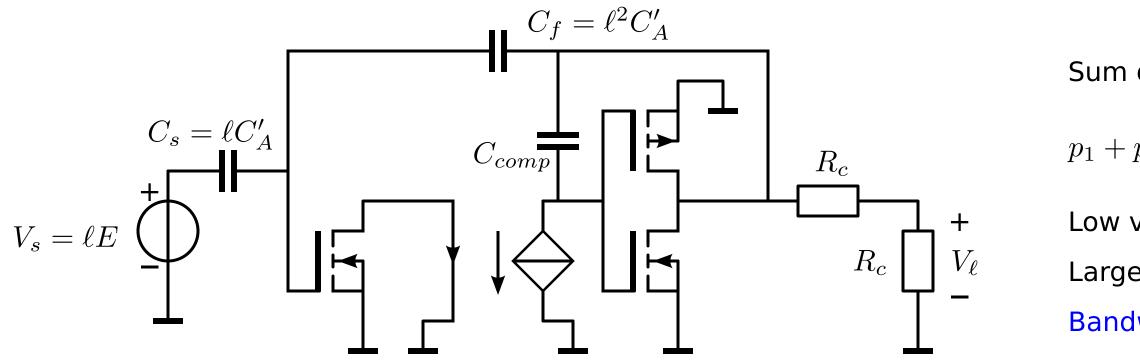
Low voltage gain in the second stage:



Sum of the poles should be increased:

 $p_1 + p_2 = p_2 = -\sqrt{2}B_f$ 

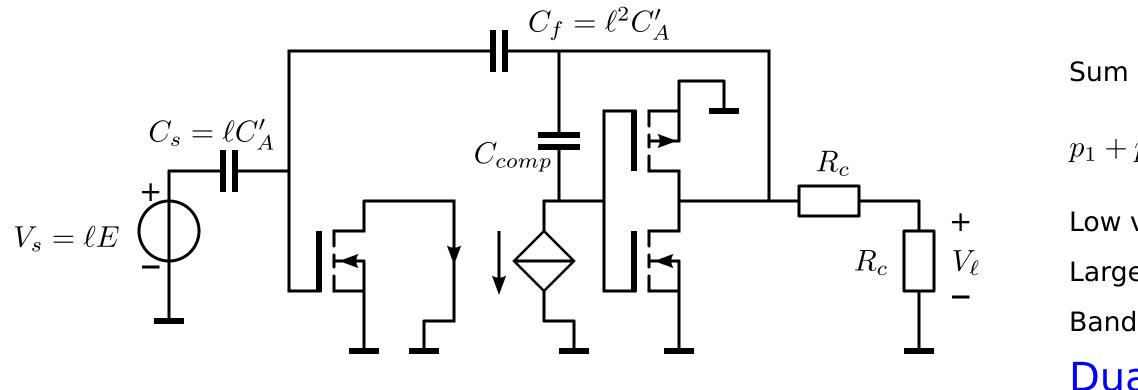
Low voltage gain in the second stage: Large compensation capacitance:



Sum of the poles should be increased:

 $p_1 + p_2 = p_2 = -\sqrt{2}B_f$ 

Low voltage gain in the second stage: Large compensation capacitance: Bandwidth reduction!



Sum of the poles should be increased:

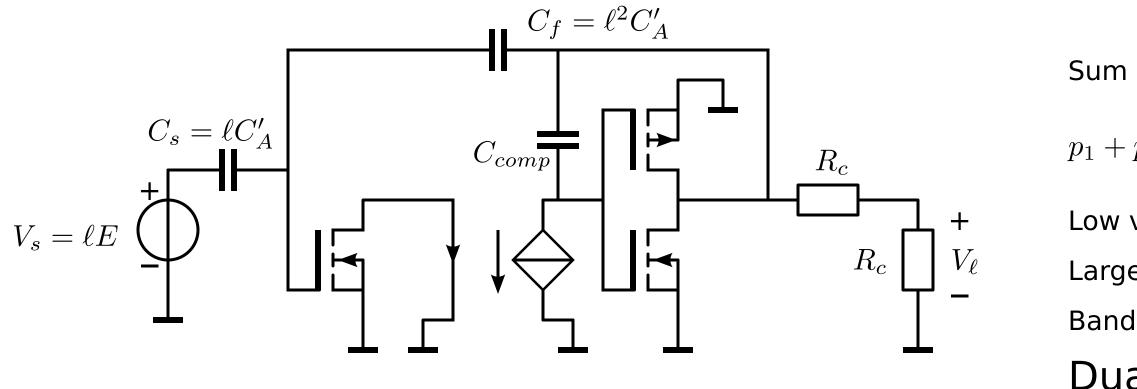
 $p_1 + p_2 = p_2 = -\sqrt{2}B_f$ 

Low voltage gain in the second stage:

Large compensation capacitance:

Bandwidth reduction!

DualStagePS.py



Sum of the poles should be increased:

 $p_1 + p_2 = p_2 = -\sqrt{2}B_f$ 

Low voltage gain in the second stage:

Large compensation capacitance:

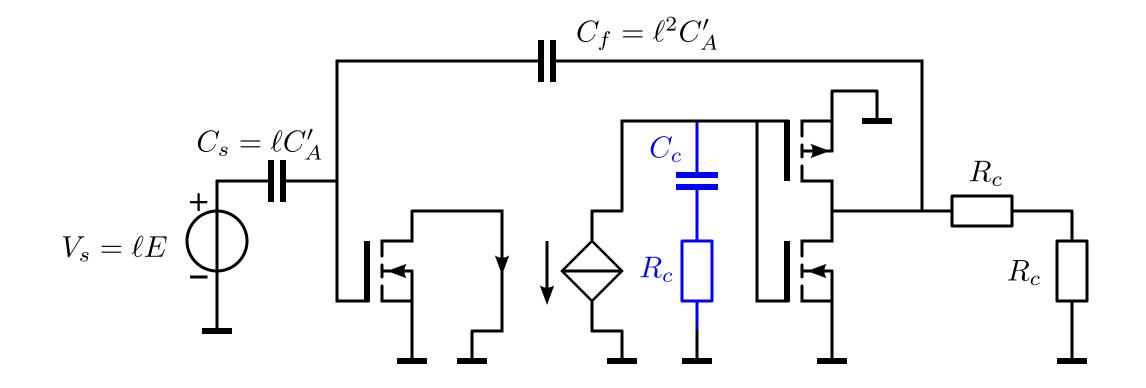
Bandwidth reduction!

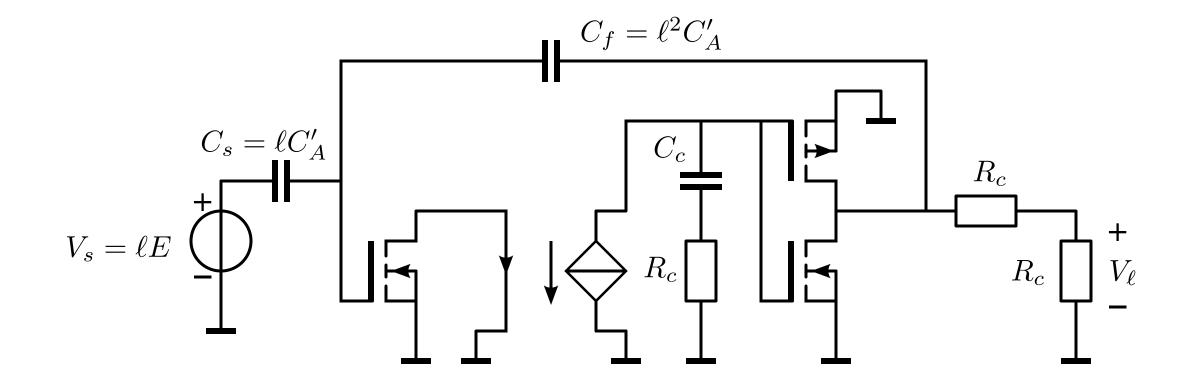
#### DualStagePS.py

#### **Structured Electronic Design**

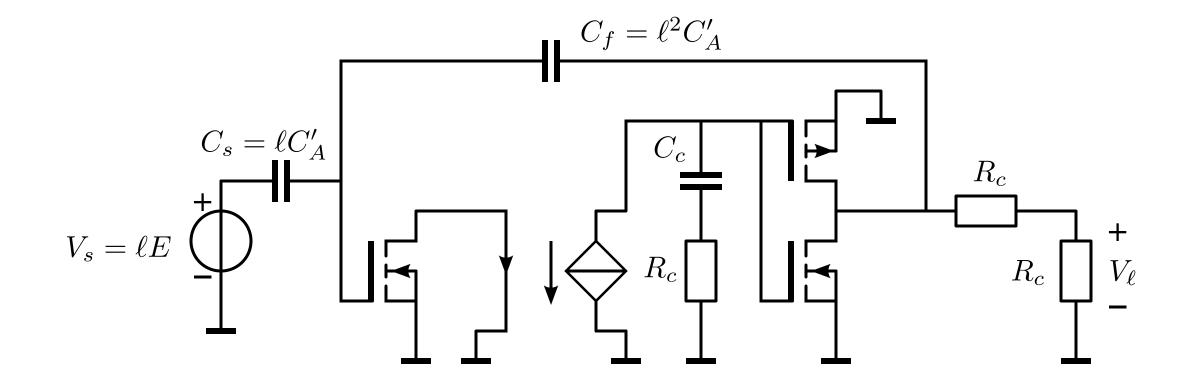
### EE4109 Pole-zero canceling

Anton J.M. Montagne

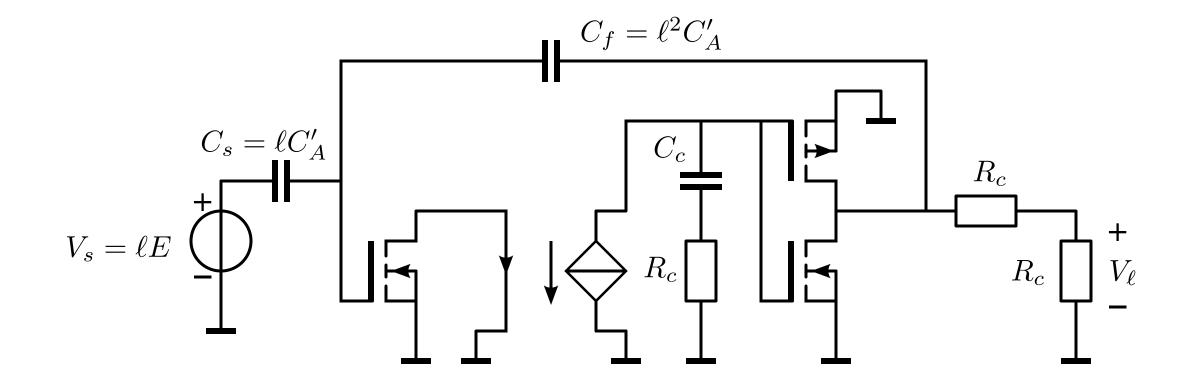




Bring the dominant pole closer to the origin

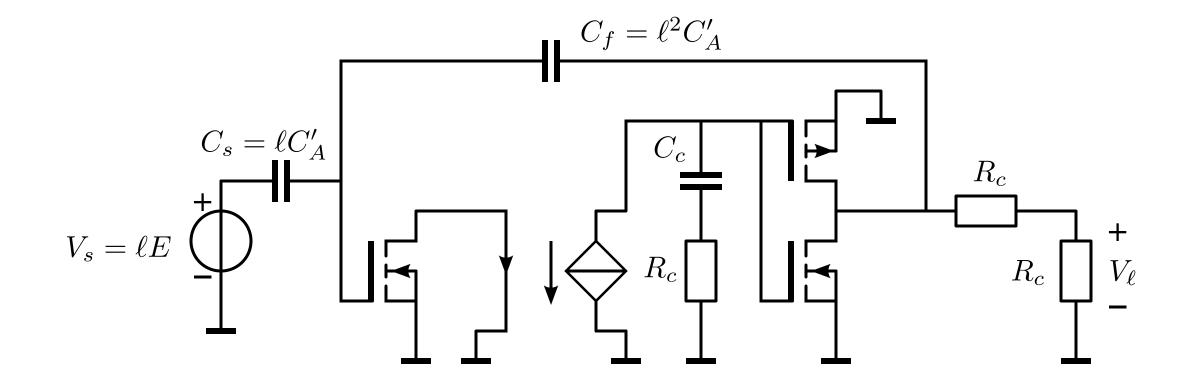


Bring the dominant pole closer to the origin or: Reduce the gain at the dominant pole



Bring the dominant pole closer to the origin or: Reduce the gain at the dominant pole

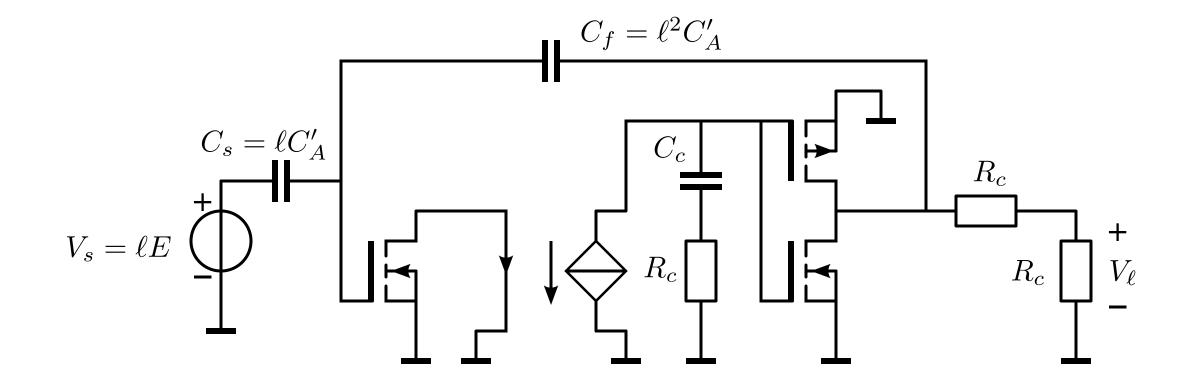
Insert a zero on the second pole:



Bring the dominant pole closer to the origin or: Reduce the gain at the dominant pole

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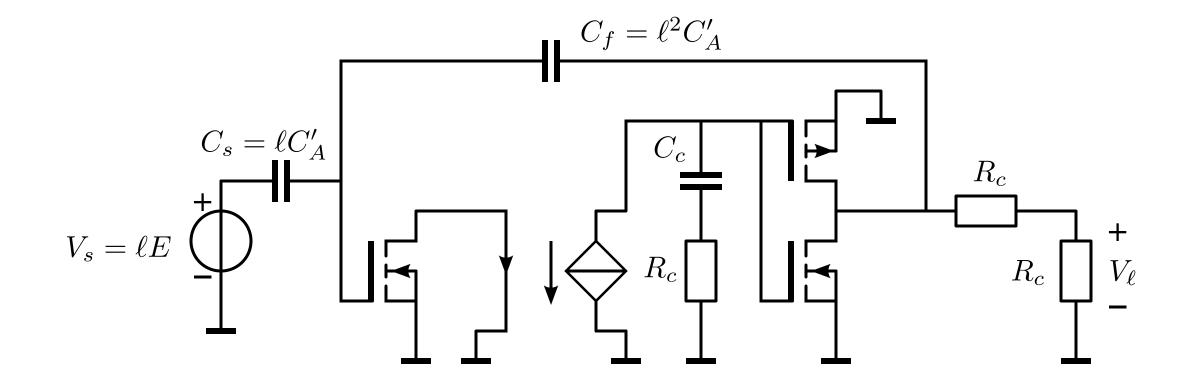
$$z = p_2 = -\frac{1}{2\pi R_c C_c}$$



Bring the dominant pole closer to the origin or: Reduce the gain at the dominant pole

Insert a zero on the second pole:

$$z = p_2 = -\frac{1}{2\pi R_c C_c}$$
$$\frac{C_c + c_{iss_2}}{c_{iss_2}} = \frac{\sqrt{2}B_f}{-p_2}$$

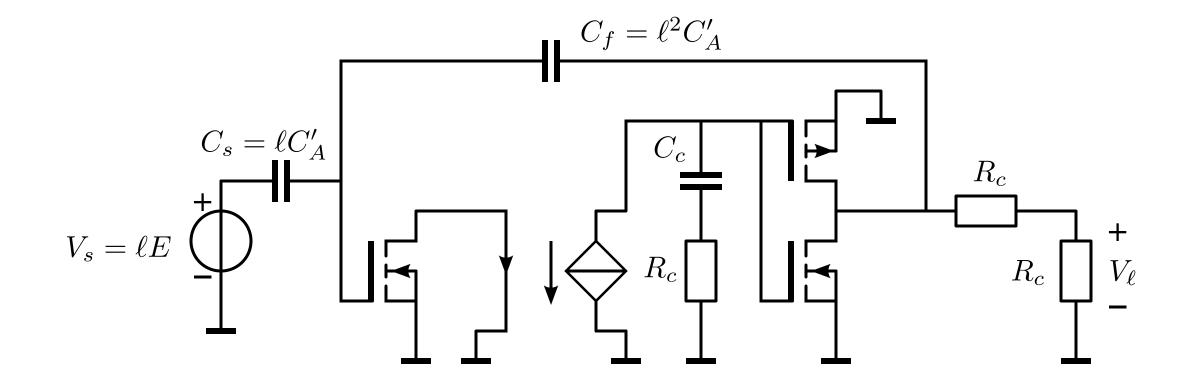


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A new pole is introduced at:



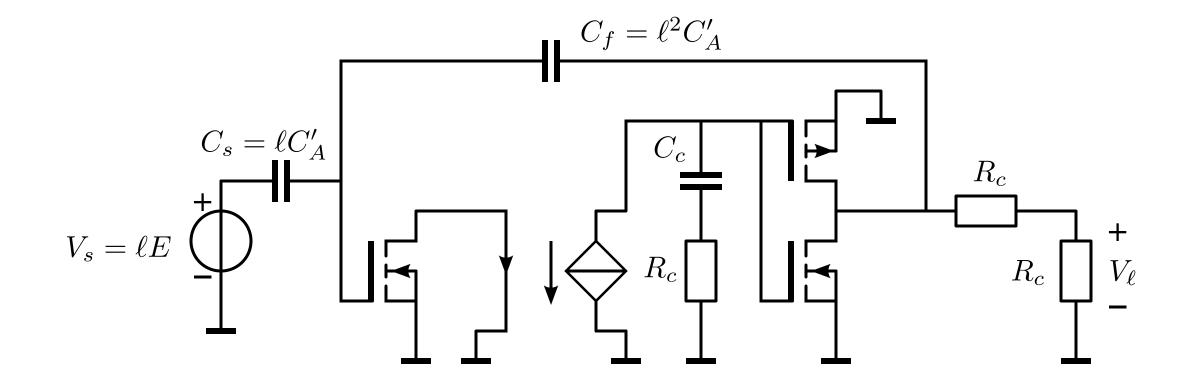
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A new pole is introduced at:

$$p_2 \approx -\frac{1}{2\pi R_c C_{iss_2}}$$



Bring the dominant pole closer to the origin or: Reduce the gain at the dominant pole

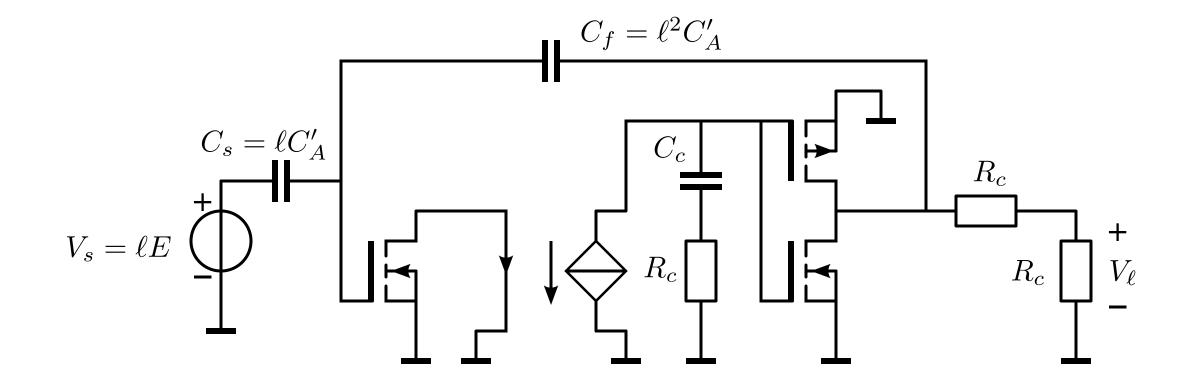
Insert a zero on the second pole:

$$z = p_2 = -\frac{1}{2\pi R_c C_c}$$
$$\frac{C_c + c_{iss_2}}{c_{iss_2}} = \frac{\sqrt{2}B_f}{-p_2}$$

A new pole is introduced at:

$$p_2 \approx -\frac{1}{2\pi R_c C_{iss_2}}$$

#### DualStagePZcancel.py



Bring the dominant pole closer to the origin or: Reduce the gain at the dominant pole

Insert a zero on the second pole:

$$z = p_2 = -\frac{1}{2\pi R_c C_c}$$
$$\frac{C_c + c_{iss_2}}{c_{iss_2}} = \frac{\sqrt{2}B_f}{-p_2}$$

A new pole is introduced at:

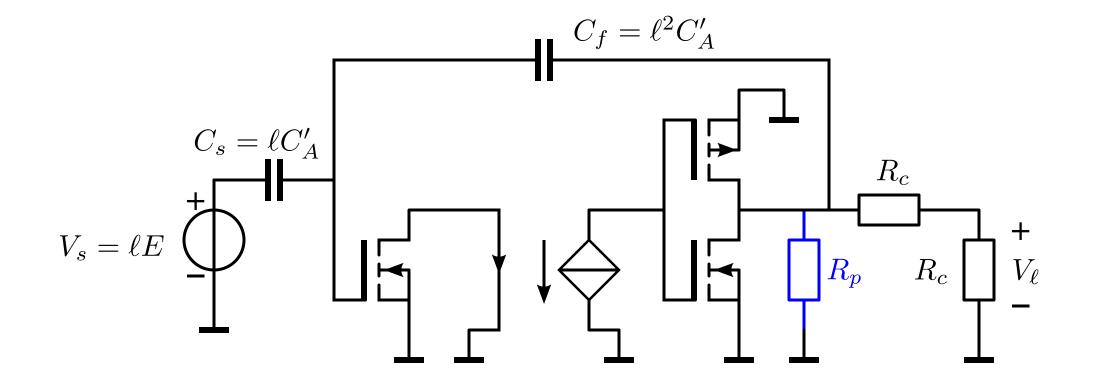
$$p_2 \approx -\frac{1}{2\pi R_c C_{iss_2}}$$

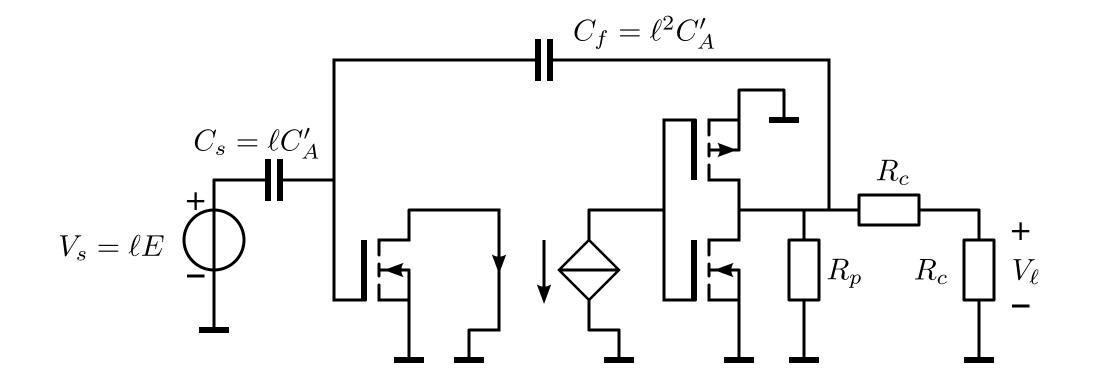
#### DualStagePZcancel.py

## **Structured Electronic Design**

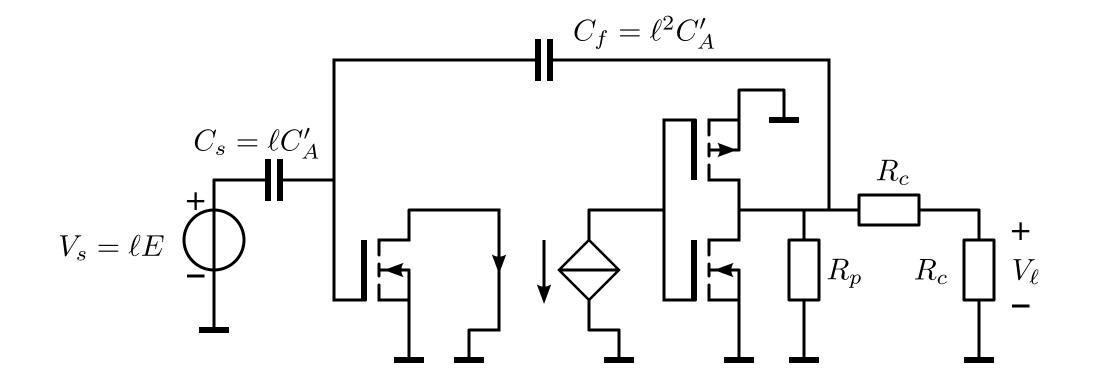
EE4109 Resistive broadbanding

Anton J.M. Montagne



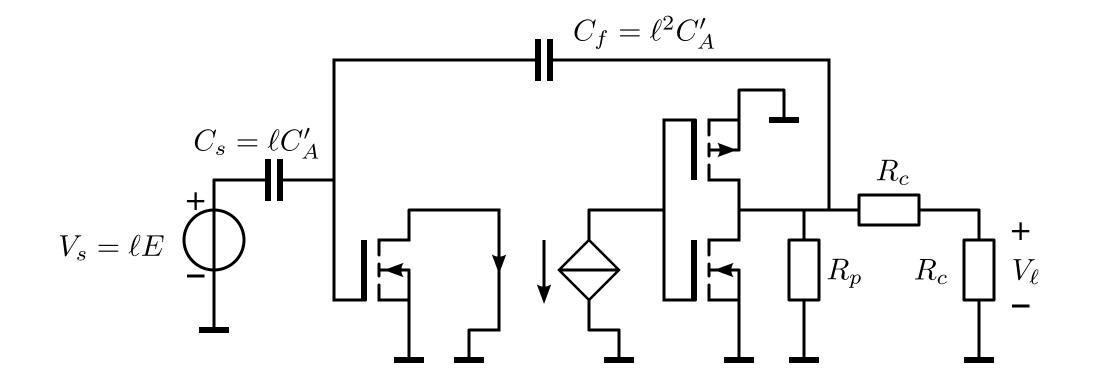


#### Increase frequency of the pole with the highest frequency.



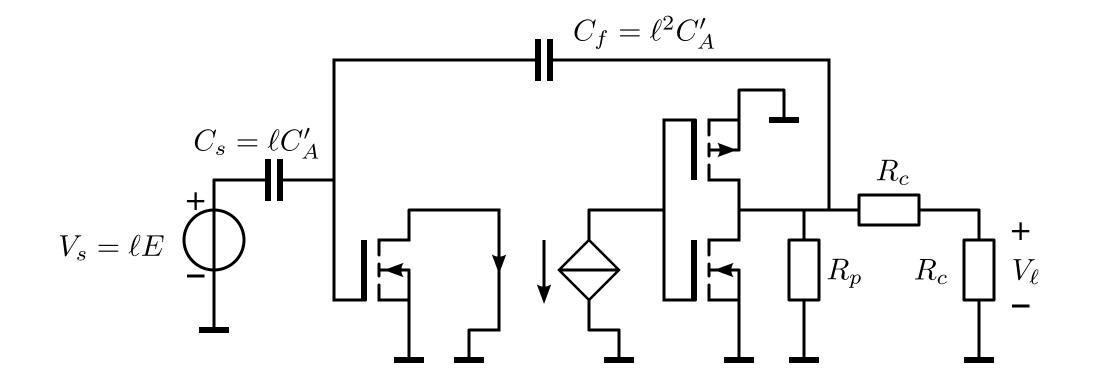
#### Increase frequency of the pole with the highest frequency.

Insert a resistor in parallel with a capacitor



#### Increase frequency of the pole with the highest frequency.

Insert a resistor in parallel with a capacitor Insert a resistor in series with an inductor

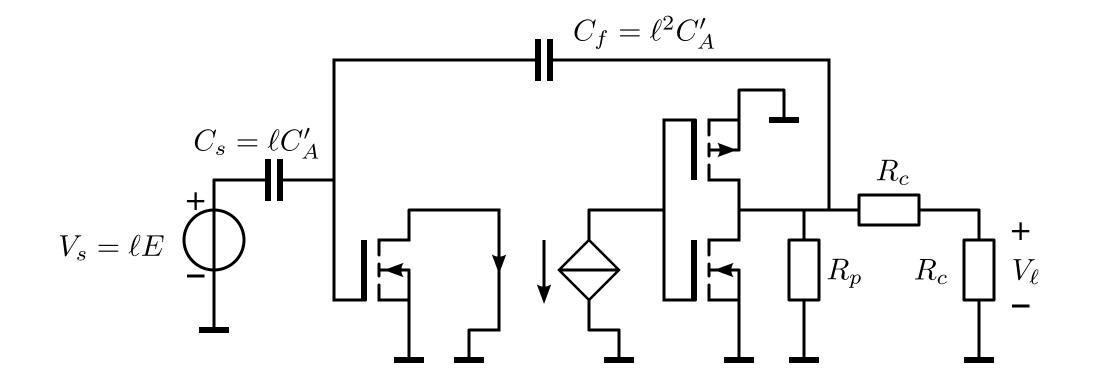


Increase frequency of the pole with the highest frequency.

Insert a resistor in parallel with a capacitor

Insert a resistor in series with an inductor

The low-frequency loop gain is reduced



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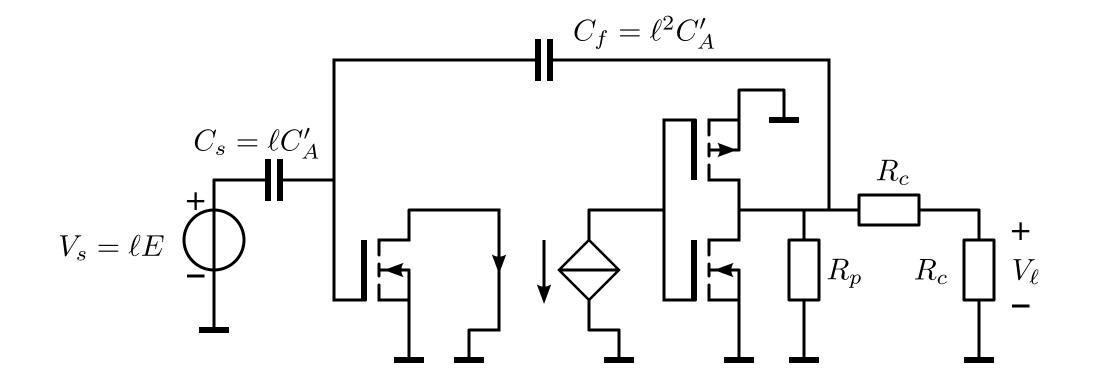
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 $\frac{R_p}{R_p + 2R_c} = -\frac{p_2}{\sqrt{2}B_f}$ 

### Resistive broadbanding



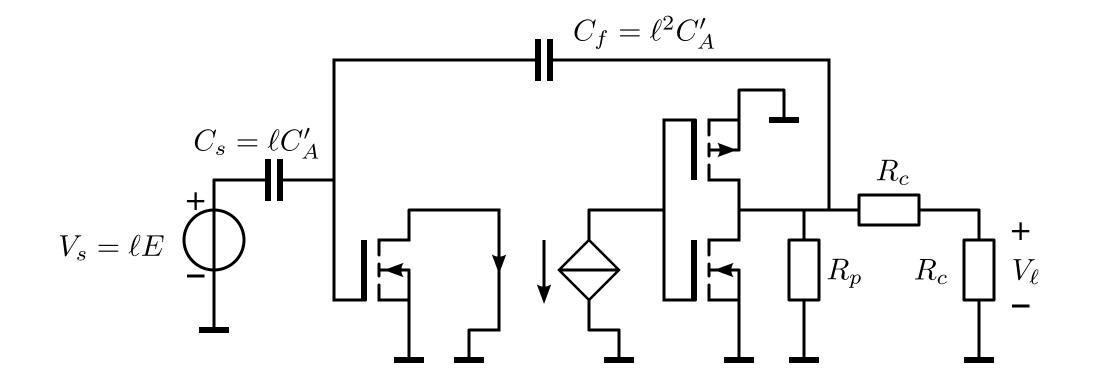
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### Resistive broadbanding



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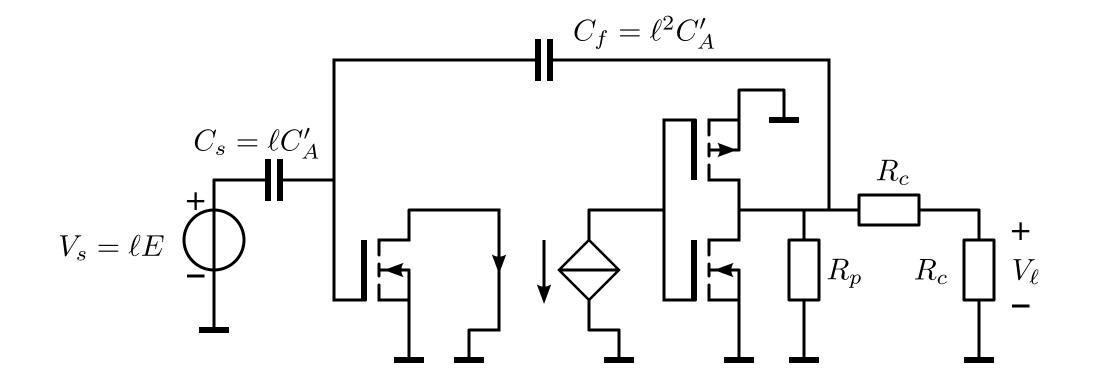
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DualStageRBB.py

### **Resistive broadbanding**



Increase frequency of the pole with the highest frequency.

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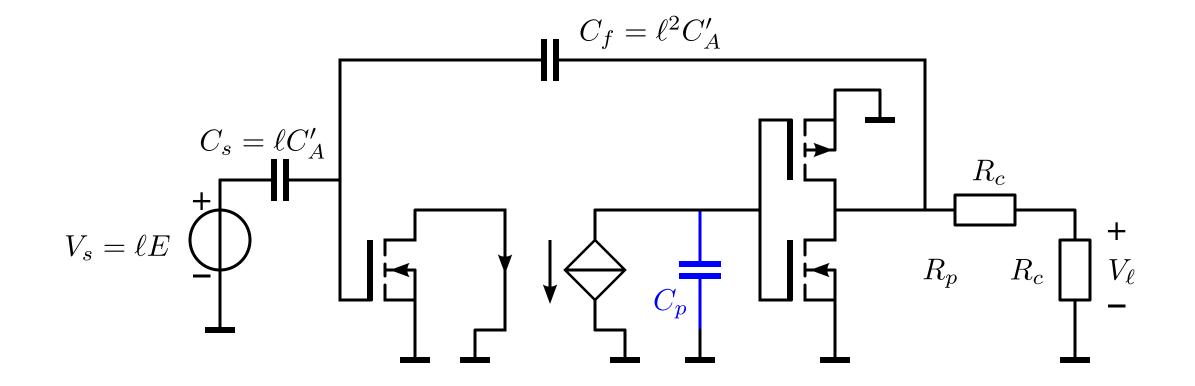
### DualStageRBB.py

# **Structured Electronic Design**

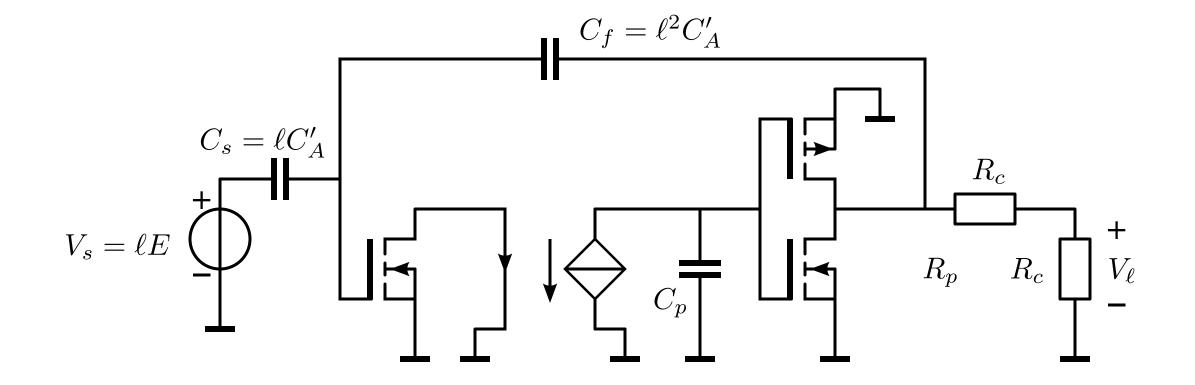
# EE4109 Bandwidth reduction

Anton J.M. Montagne

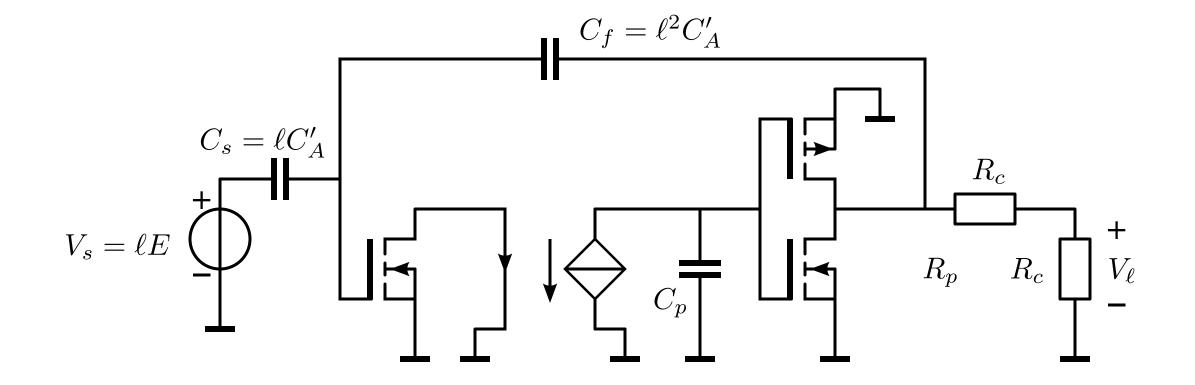
(c) 2020 A.J.M. Montagne 77



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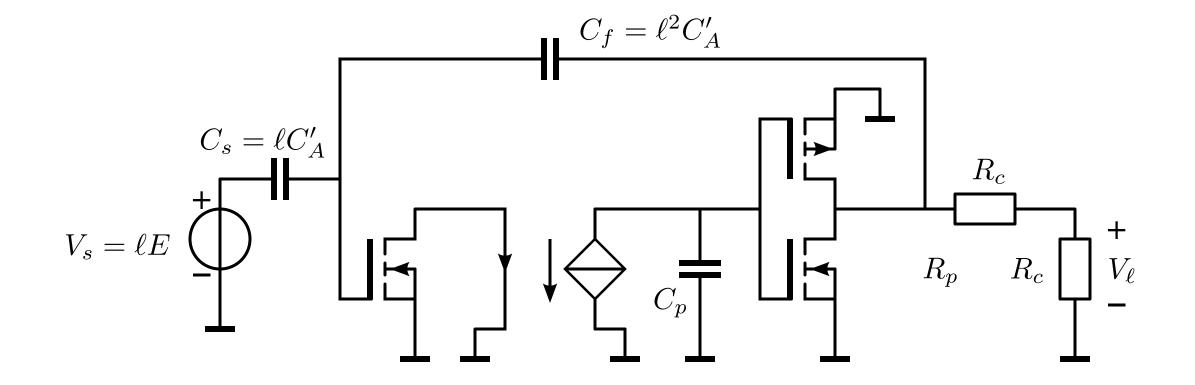


Decrease the frequency of the pole with the lowest frequency such that the second pole is at -sqrt(2) times the bandwidth.



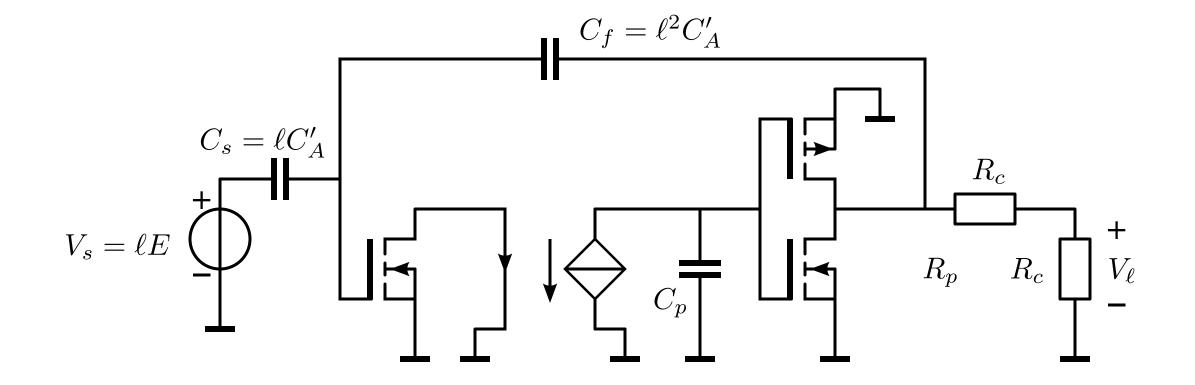
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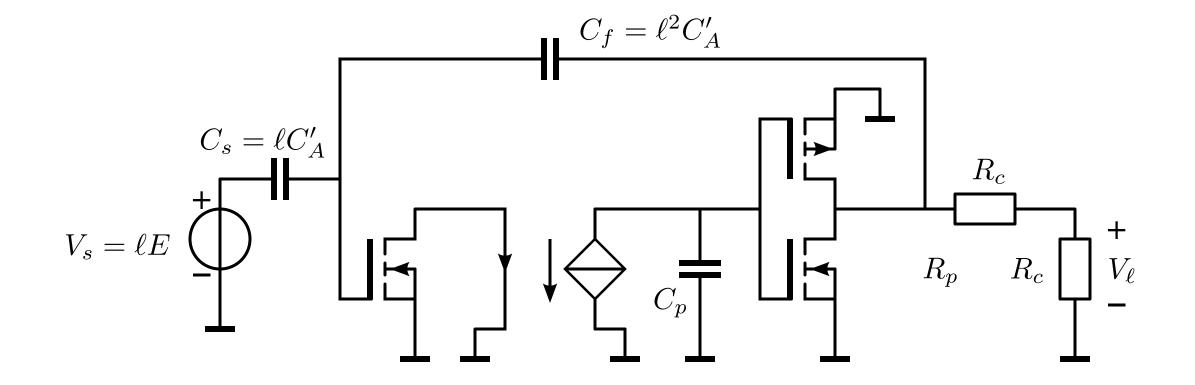


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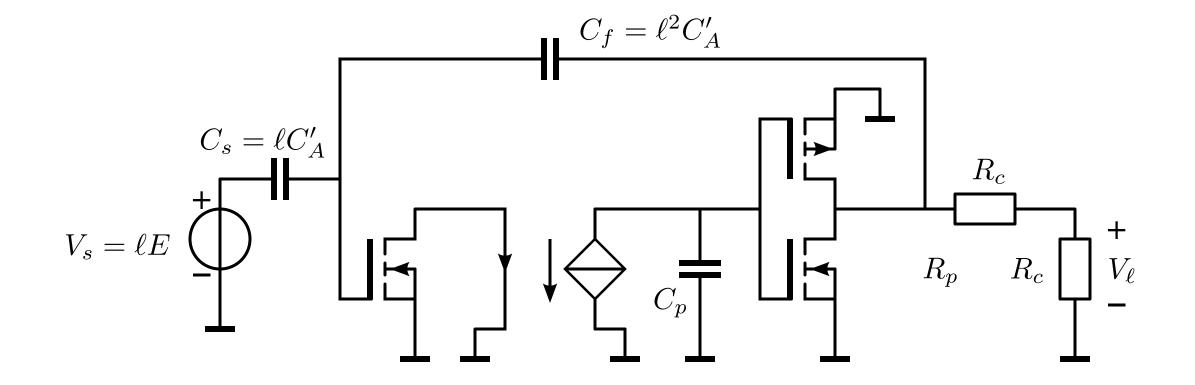
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$$C_p = \left(2\left(\frac{B_f}{p_2}\right)^2 - 1\right)c_{iss_2}$$

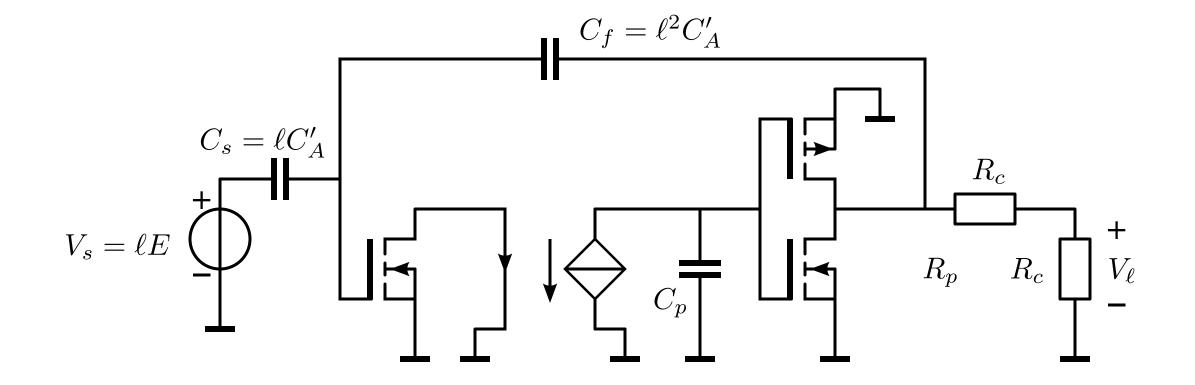


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DualStageBR.py



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DualStageBR.py

# **Structured Electronic Design**

EE4109 Active antenna frequency compensation conclusions

Anton J.M. Montagne

Bandwidth is more than required

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Coming next:

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Bandwidth limitation with phantom-zeros

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