# **Structured Electronic Design**

**Design Procedure** Negative Feedback Amplifier Types

Anton J.M. Montagne



## **Starting point** We would like to establish a transfer:



 $+ V_{\ell}$  $Z_\ell$ 

1. Measure the load signal (V or I)

1. Measure the load signal (V or I)

a. Voltage should be measured across (in parallel with) the load

1. Measure the load signal (V or I)

a. Voltage should be measured across (in parallel with) the load

b. Current should be measured through (in series with) the load

bad bad



╋

 $Z_{\ell}$ 

### **Starting point** We would like to establish a transfer:



1. Measure the voltage across the load

(c) 2021 A.J.M. Montagne 7



+

 $V_{\ell}$ 

 $Z_{\ell}$ 

+

 $V_{\ell}$ 

### **Starting point** We would like to establish a transfer:



1. Measure the voltage across the load

(c) 2021 A.J.M. Montagne 8

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- 2. Design a network that generates of copy of the source signal (V or I) from the measured load signal

bad bad

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- 2. Design a network that generates of copy of the source signal (V or I) from the measured load signal

a. The transfer of this network is the reciprocal of the desired source-to-load transfer

+



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

+ $V_{\ell}$ 

 $Z_{\ell}$ 



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

+ $V_{\ell}$ 

 $Z_\ell$ 

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- 2. Design a network that generates of copy of the source signal (V or I) from the measured load signal a. The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the loadb. Current should be measured through (in series with) the load
- Design a network that generates of copy of the source signal (V or I) from the measured load signal
  The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the loadb. Current should be measured through (in series with) the load
- Design a network that generates of copy of the source signal (V or I) from the measured load signal
  The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

╉

 $Z_\ell$ 

3. Subtract the copy from the source voltage



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

╉

 $Z_{\ell}$ 

3. Subtract the copy from the source voltage

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the loadb. Current should be measured through (in series with) the load
- Design a network that generates of copy of the source signal (V or I) from the measured load signal
  The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel
- 4. Nullify the difference

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- 2. Design a network that generates of copy of the source signal (V or I) from the measured load signal a. The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel
- 4. Nullify the difference

a. In case of a voltage source signal, a nullator closes the loop of the above anti-series connection

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- Design a network that generates of copy of the source signal (V or I) from the measured load signal
  The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel
- 4. Nullify the difference
  - a. In case of a voltage source signal, a nullator closes the loop of the above anti-series connectionb. In case of a current source signal, a nullator is placed in parallel with the above anti-parallel
  - b. In case of a current source signal, a nullator is placed in pa connection



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

╉

 $Z_\ell$ 

- 3. Subtract the copy from the source voltage
- 4. Nullify the difference



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

╉

 $Z_\ell$ 

- 3. Subtract the copy from the source voltage
- 4. Nullify the difference

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the loadb. Current should be measured through (in series with) the load
- Design a network that generates of copy of the source signal (V or I) from the measured load signal
  The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel
- 4. Nullify the difference
  - a. In case of a voltage source signal, a nullator closes the loop of the above anti-series connection b. In case of a current source signal, a nullator is placed in parallel with the above anti-parallel
  - b. In case of a current source signal, a nullator is placed in pa connection
  - c. In case of a voltage load signal, a norator is placed in parallel with the load

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- 2. Design a network that generates of copy of the source signal (V or I) from the measured load signal a. The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel
- 4. Nullify the difference
  - a. In case of a voltage source signal, a nullator closes the loop of the above anti-series connection b. In case of a current source signal, a nullator is placed in parallel with the above anti-parallel
  - connection
  - c. In case of a voltage load signal, a norator is placed in parallel with the load
  - d. In case of a current load signal, a norator closes the loop of the series connection of the load and the input of the feedback network



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

╉

 $Z_\ell$ 

- 3. Subtract the copy from the source voltage
- 4. Nullify the difference



### **Starting point** We would like to establish a transfer:



- 1. Measure the voltage across the load
- 2. Generate a copy of the source voltage from  $V_\ell$

╉

 $Z_\ell$ 

- 3. Subtract the copy from the source voltage
- 4. Nullify the difference



 $\left] Z_{\ell} \quad \overset{+}{V}_{\ell} \right]$ 

- 1. Measure the load signal (V or I)
  - a. Voltage should be measured across (in parallel with) the load b. Current should be measured through (in series with) the load
- 2. Design a network that generates of copy of the source signal (V or I) from the measured load signal a. The transfer of this network is the reciprocal of the desired source-to-load transfer
- 3. Subtract the copy from the source signal
  - a. In case of a voltage source signal, the signal source and the output of the feedback network should be connected anti-series
  - b. In case of a current source signal, the signal source and the output of the feedback network should be connected anti-parallel
- 4. Nullify the difference
  - a. In case of a voltage source signal, a nullator closes the loop of the above anti-series connection b. In case of a current source signal, a nullator is placed in parallel with the above anti-parallel
  - connection
  - c. In case of a voltage load signal, a norator is placed in parallel with the load
  - d. In case of a current load signal, a norator closes the loop of the series connection of the load and the input of the feedback network