## **Structured Electronic Design**

Balancing of two-terminal elements

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Anti-series connection of equal elements



Anti-series connection of equal elements



Anti-series connection of equal elements





Anti-series connection of equal elements















Anti-series connection of equal elements













#### Elements can be biased with common-mode current sources





#### Elements can be biased with common-mode current sources Bias voltage appears as common-mode voltage in the circuit





#### Elements can be biased with common-mode current sources Bias voltage appears as common-mode voltage in the circuit

Small-signal model











Small-signal impedance is



Small-signal impedance is



Small-signal impedance is

the quiescent operating point





Small-signal impedance is

the quiescent operating point





Small-signal impedance is  $\begin{bmatrix} Z = Z_1 + Z_2 \\ C \end{bmatrix} = Z_1 + Z_2$  impedances of constituting elements

the quiescent operating point







Small-signal impedance is  $\int Z = Z_1 + Z_2$  sum of small-signal impedances of constituting elements

the quiescent operating point







Anti-parallel connection of equal elements





Anti-parallel connection of equal elements





Anti-parallel connection of equal elements







Anti-parallel connection of equal elements







Anti-parallel connection of equal elements







Anti-parallel connection of equal elements







 $+ i_p$  $v_p$ 







Elements can be biased with common-mode voltage sources





Elements can be biased with common-mode voltage sources Bias current appears as common-mode current in the circuit





Elements can be biased with common-mode voltage sources Bias current appears as common-mode current in the circuit







Small-signal admittance is



Small-signal admittance is

 $Y = 2Y_1 = 2Y_2$ 



Small-signal admittance is

 $Y = 2Y_1 = 2Y_2$ 

Twice the admittance in the quiescent operating point



Small-signal admittance is

 $Y = 2Y_1 = 2Y_2$ 

Twice the admittance in the quiescent operating point



 $\begin{array}{c} \mbox{Small-signal admittance is}\\ Y=Y_1+Y_2 & \mbox{admittances of the}\\ \mbox{constituting elements} \end{array} \end{array}$ 

 $Y = 2Y_1 = 2Y_2$ 

Twice the admittance in the quiescent operating point





Small-signal admittance is sum of small-signal constituting elements

 $Y = 2Y_1 = 2Y_2$ 

Twice the admittance in the quiescent operating point





Small-signal admittance is constituting elements

 $Y = 2Y_1 = 2Y_2$ 

Twice the admittance in the quiescent operating point

