Structured Electronic Design

Intrinsic CS stage: Equivalent-input noise sources

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ignore right half-plane zero





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it's above the cut-off frequency ω_T





ignore right half-plane zero

it's above the cut-off frequency ω

 $z=rac{g_m}{c_{dg}}$ $\omega_T=rac{g_m}{c_{gs}}$

 $c_{dg} \ll c_{gs}$



ignore right half-plane zero

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$$B = -\frac{1}{g_m}$$

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Two correlated noise sources!

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v_n : total source-referred noise





 $S_{v_n} = \left| B + DZ_s \right|^2 S_{id}$



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v_n : total source-referred noise

$$\left|Z_{s}\right|^{2}S_{ia}$$







i_n : total source-referred noise



 $S_{v_n} = \left| BY_s + D \right|^2 S_{id}$

i_n : total source-referred noise



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i_n : total source-referred noise