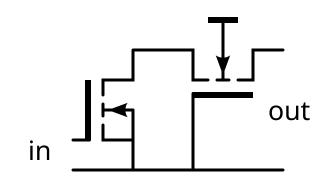
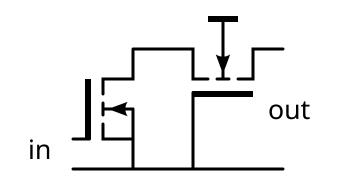
Structured Electronic Design Preferred Controller Stages

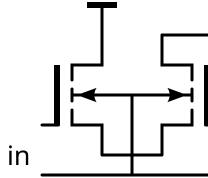
inverting CS-CG cascode stage



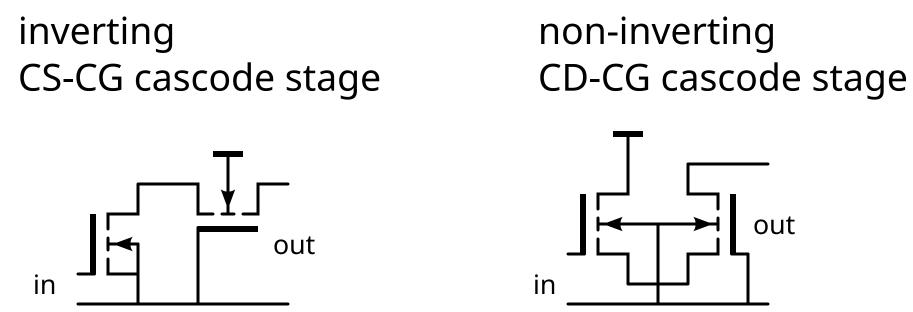


non-inverting

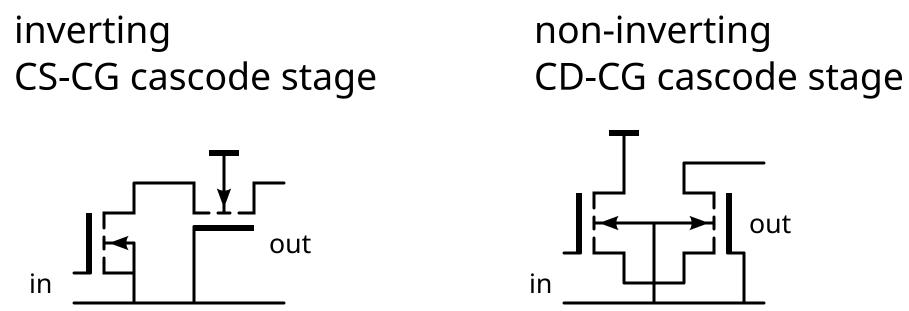




CD-CG cascode stage

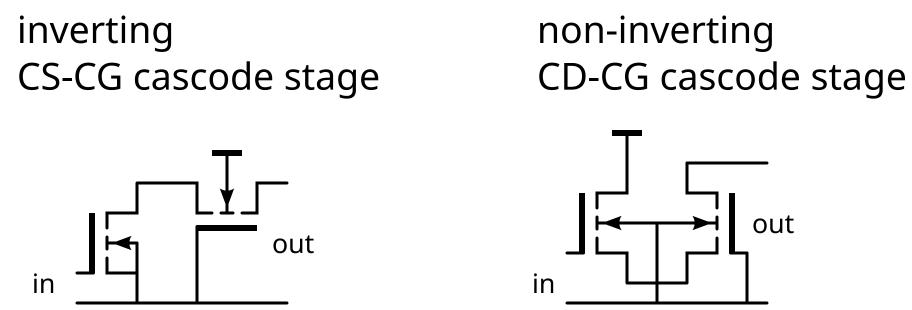


Also: anti-series and complementary-parallel versions



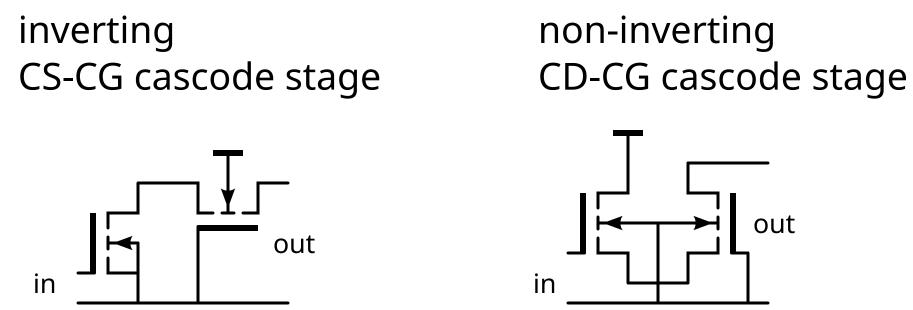
Also: anti-series and complementary-parallel versions

CS stage: Chapter 5



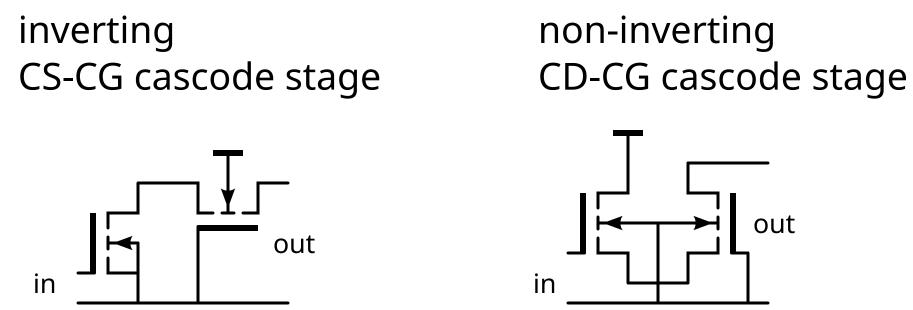
Also: anti-series and complementary-parallel versions

CS stage: Chapter 5 Balanced stages: Chapter 6



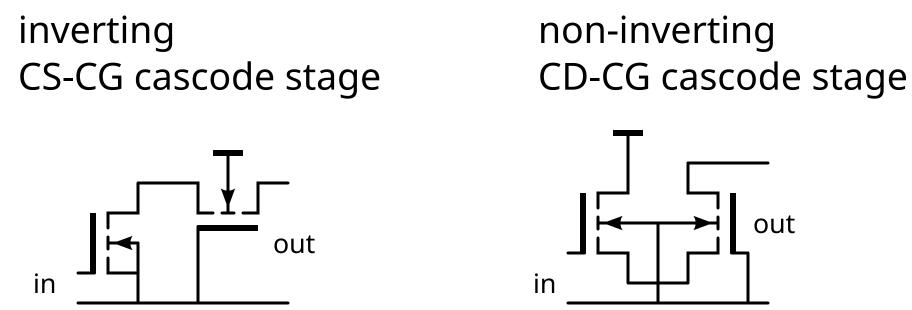
Also: anti-series and complementary-parallel versions

CS stage: Chapter 5 Balanced stages: Chapter 6 Local feedback stages: Chapter 13



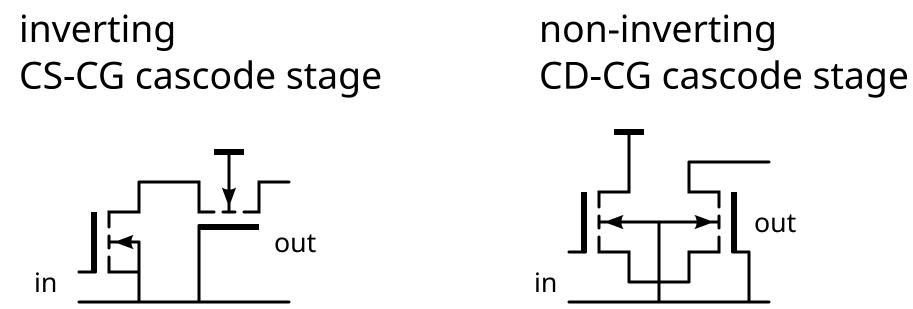
Also: anti-series and complementary-parallel versions

CS stage: Chapter 5 Balanced stages: Chapter 6 Local feedback stages: Chapter 13 Cascode stages: Chapter 14



Also: anti-series and complementary-parallel versions

CS stage: Chapter 5 Balanced stages: Chapter 6 Local feedback stages: Chapter 13 Cascode stages: Chapter 14 Design of multi-stage controllers: Chapter 14



Also: anti-series and complementary-parallel versions

CS stage: Chapter 5 Balanced stages: Chapter 6 Local feedback stages: Chapter 13 Cascode stages: Chapter 14 Design of multi-stage controllers: Chapter 14

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