

# **Structured Electronic Design**

Discover CMOS characteristics  
with jupyter notebook

*Anton J.M. Montagne*

# Discover CMOS characteristics

Structured Electronic Design [Running] - Oracle VM VirtualBox  
Start - Jupyter Notebook - Mozilla Firefox

jupyter Start Last Checkpoint: 11/02/2019 (autosaved) Logout

File Edit View Insert Cell Kernel Widgets Help Trusted Python 3

In [2]: # First we will need to import the Python - Spice interface  
import spice\_invoker as si  
  
# This is the name of the Transistor Library used, this can be replaced with "CMOS18-1.lib", "CMOS18-2.lib" or  
libname = "CMOS18TT.lib"  
  
# This is the name of the transistor model to be used by Spice, for all libraries named here this is the same  
modelname = "C18mos"  
  
# Then we can start by creating a Python - Spice interface with the above defined Library and Model!  
inv = si.LTSpiceInvoker(libname, modelname)

In [3]: # We will create the plot which is used as an example above:  
inv.Id\_Vds\_Plot(VGSmin="0", VGSmax="1.8", VGSstep="0.3", VDSmax="1.8")

Vds\_sweep

Id [A]

Vds [V]

In [4]: # Once we are done using the interface, we close it by running:  
inv.clean()

Feel free to adjust the parameters in the 'inv.Id\_Vds\_Plot()' to see what happens!

If you have altered the parameters to more extreme values, you might notice weird behavior in the resulting plot. While sometimes this can represent the actual behavior of a transistor, it can also happen that this is a quirk or incompleteness of the transistor library. After all, these transistor libraries have been designed to model transistors in specific operating regions.

**Exporting notebooks**

It is possible to export these notebooks to a PDF file, these PDF files can then be used to hand in the exercises you have made or to share the work you have done.

## Virtual Machine with Jupyter Notebook

LTspice for determination of the device characteristics

Python (matplotlib) for plotting

Jupyter Notebook user interface

Thanks to our teaching assistants  
Luc Enthoven  
Francesc Varkevisser

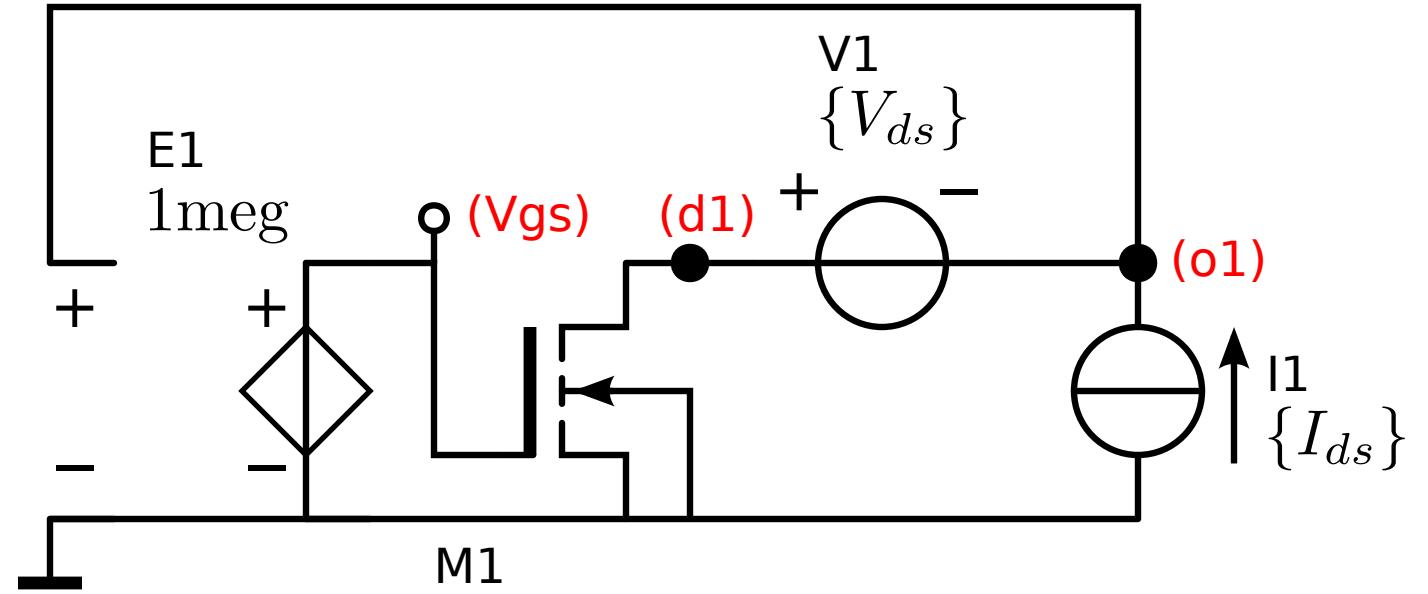
Future developments:

Integration in SLiCAP\_python  
you are invited to join the SLiCAP development team

# Determination of small-signal parameters

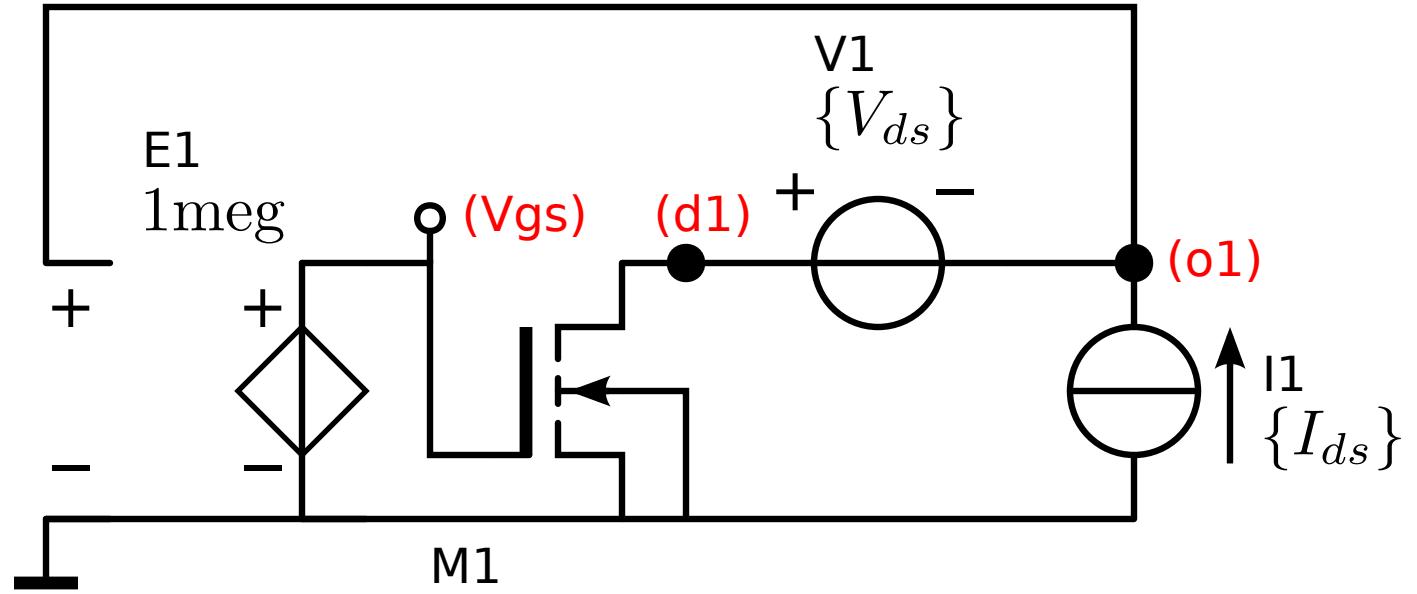
# Determination of small-signal parameters

Circuit for determination of  $V_{gs}$



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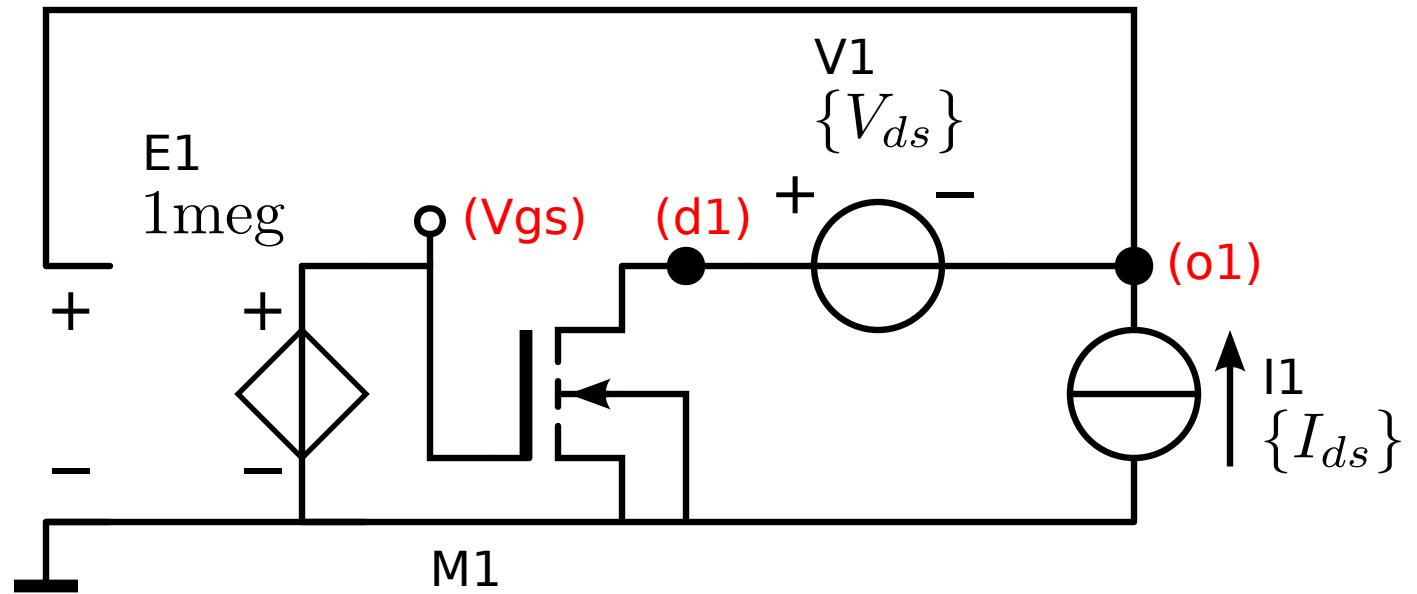
Circuit for determination of  $V_{gs}$



Determination of Y-parameters

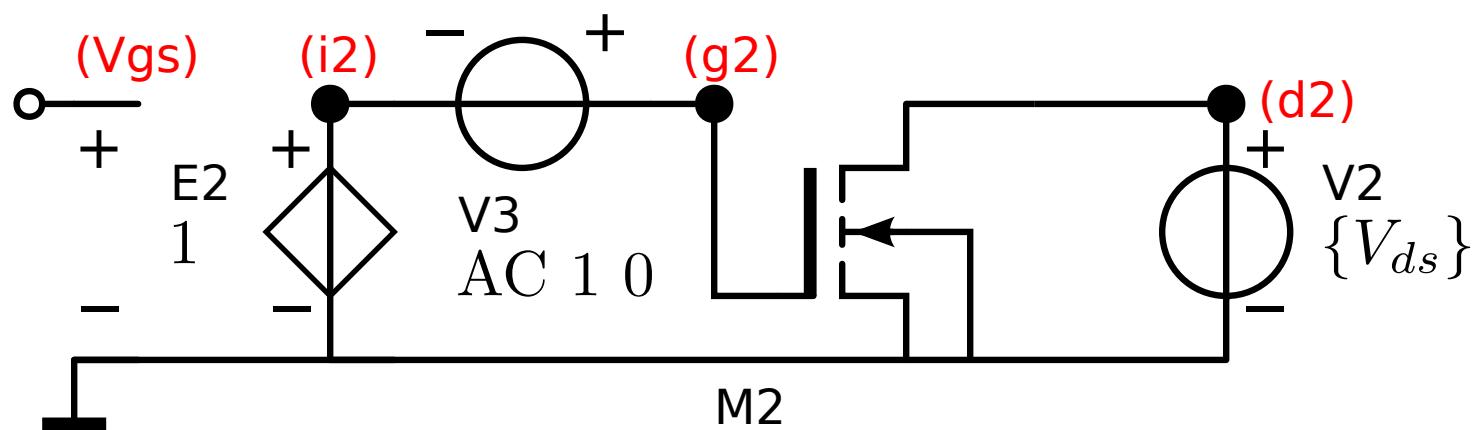
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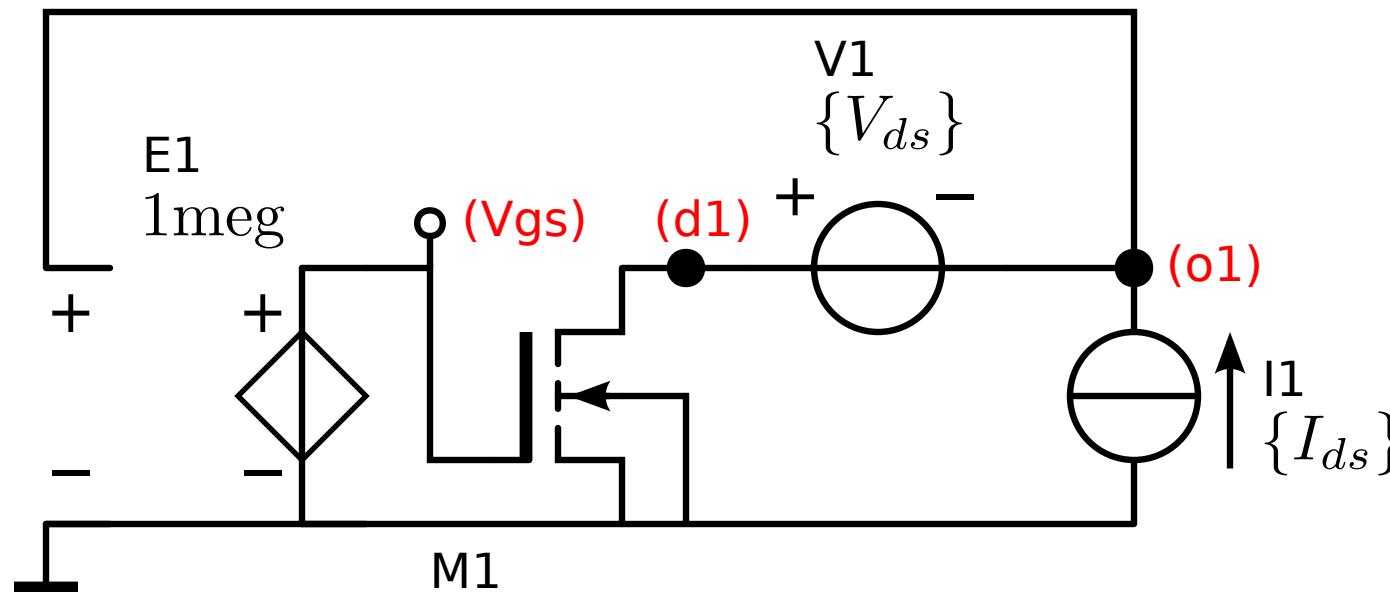
## Determination of Y-parameters

Circuit for determination of  $Y_{11}$  and  $Y_{12}$



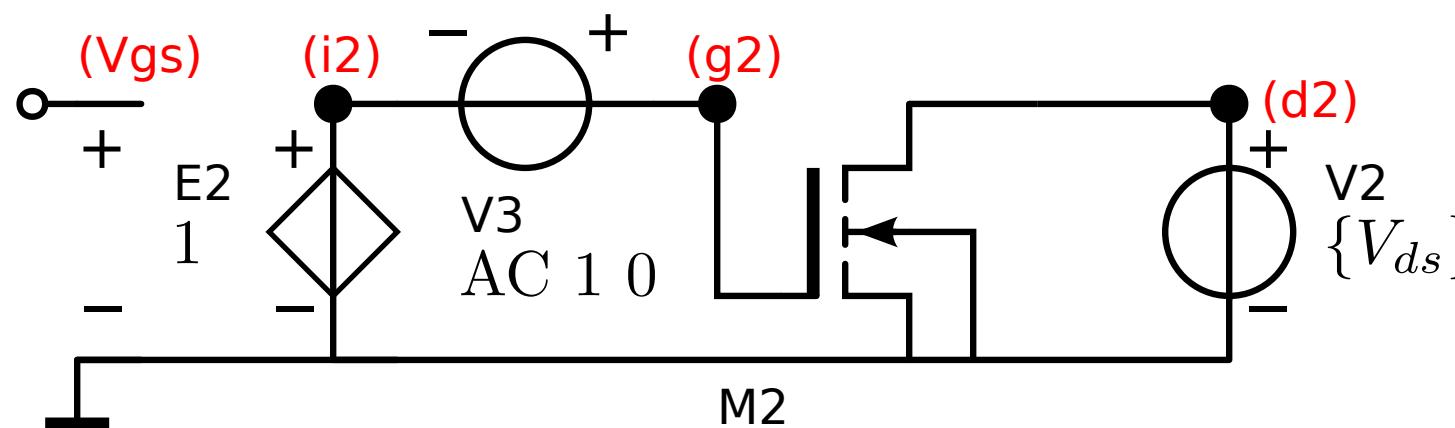
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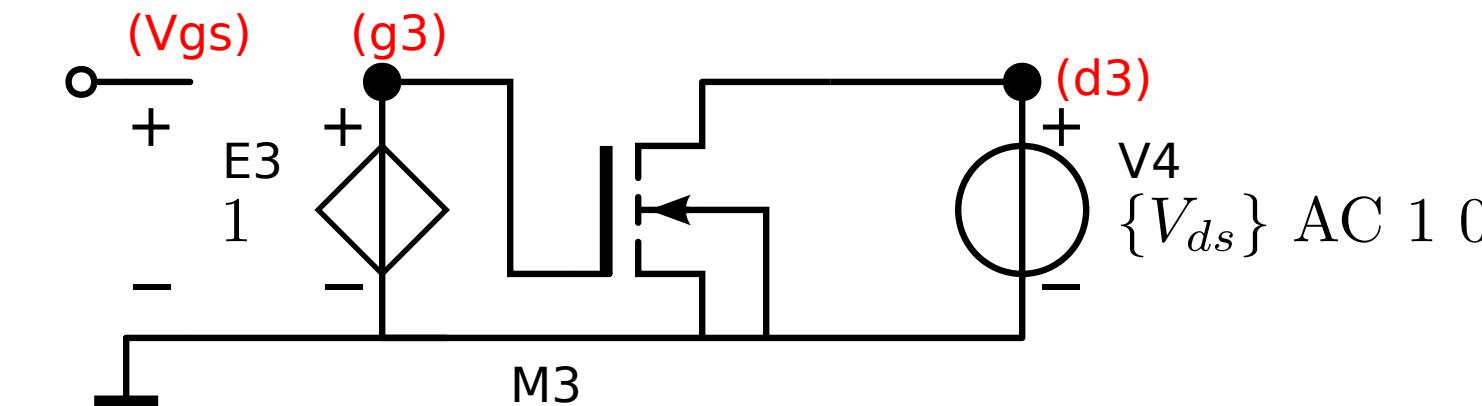


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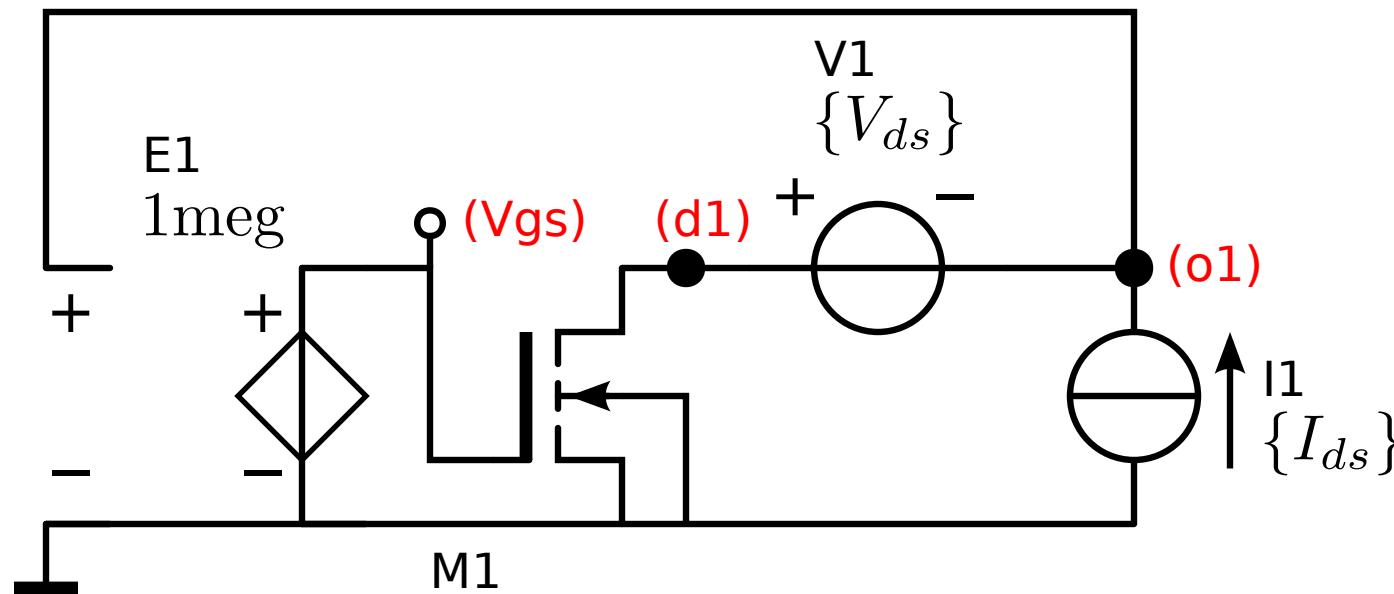


Circuit for determination of  $Y_{21}$  and  $Y_{22}$



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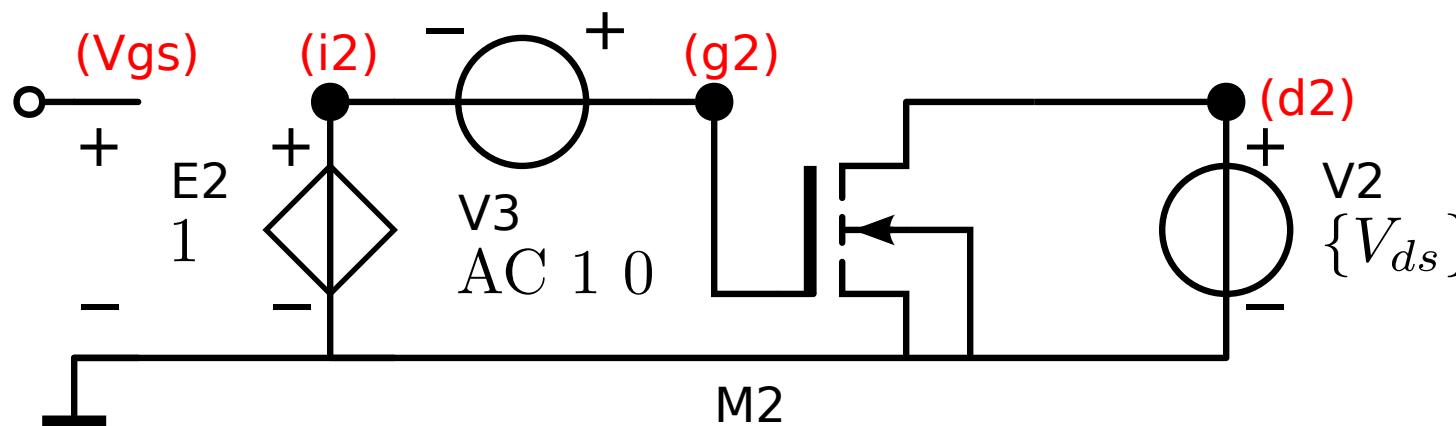
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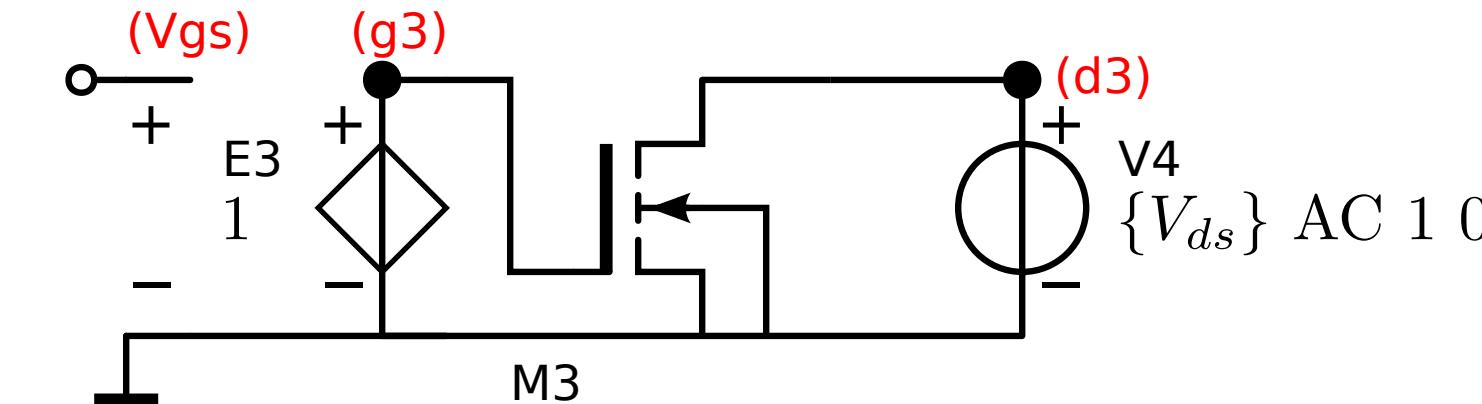
$$g_m = \text{Re}[Y_{1,2}]$$

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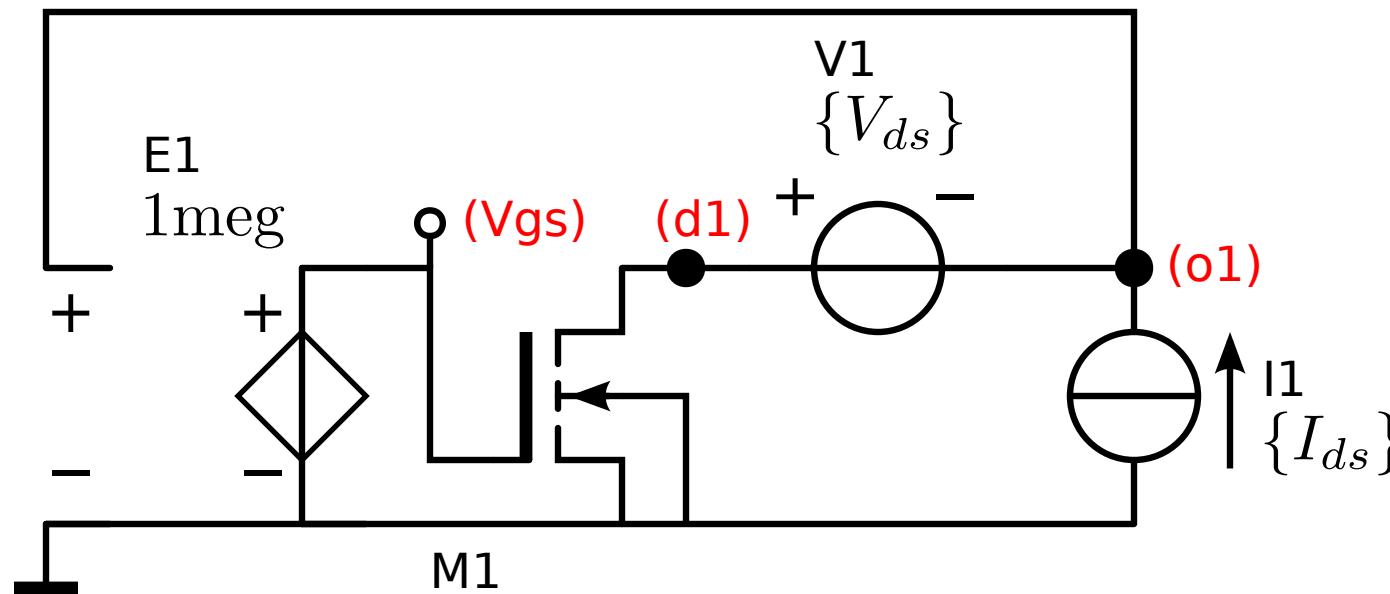


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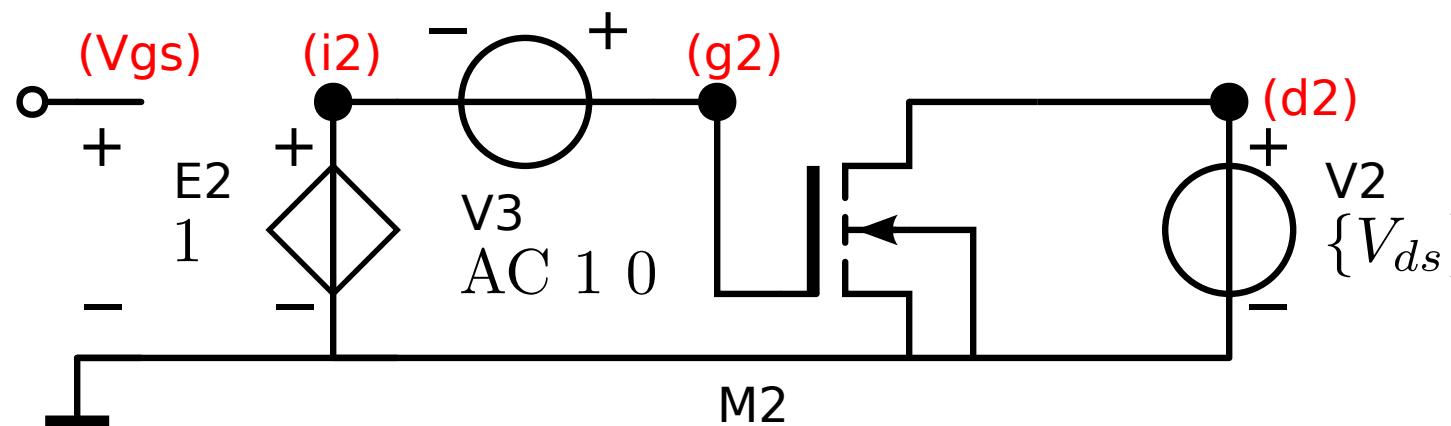


$$g_m = \text{Re} [Y_{1,2}]$$

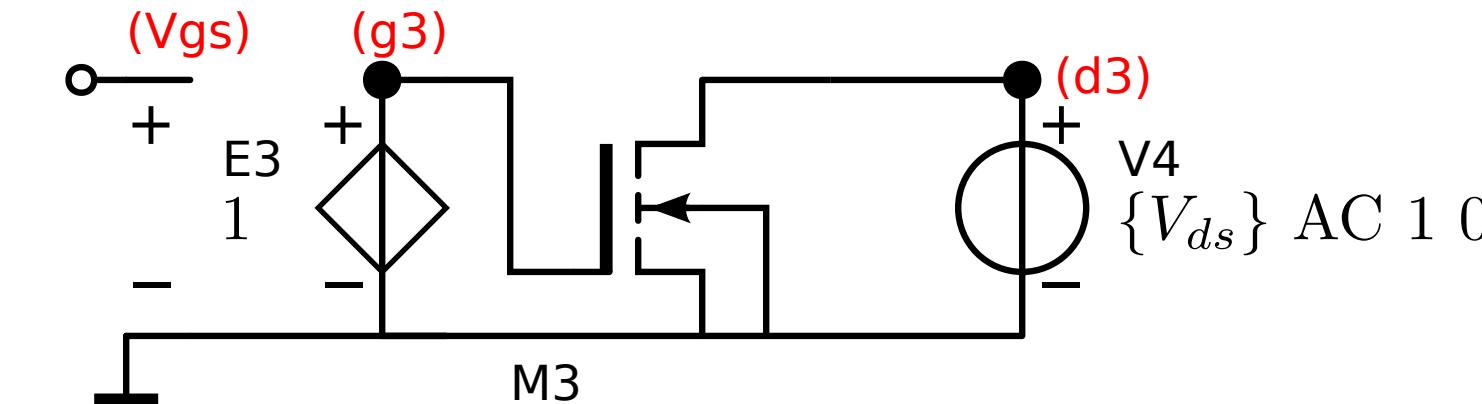
$$c_{gs} + c_{dg} = \frac{1}{2\pi f} \text{Im} [Y_{1,1}]$$

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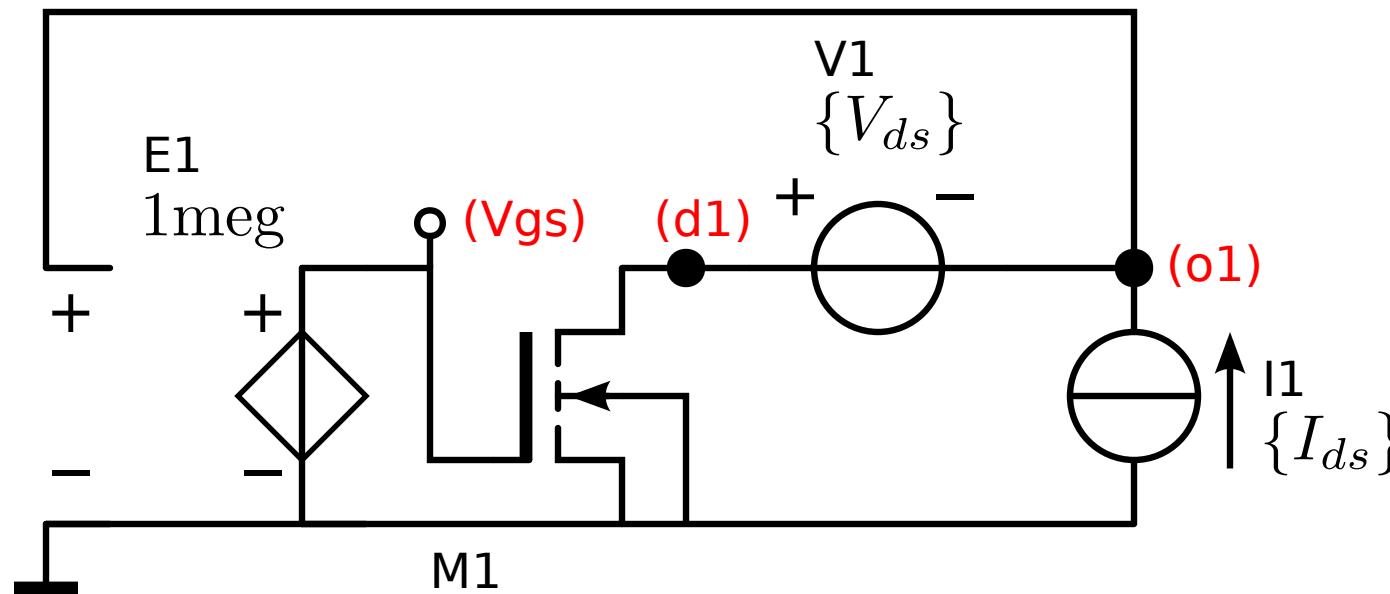


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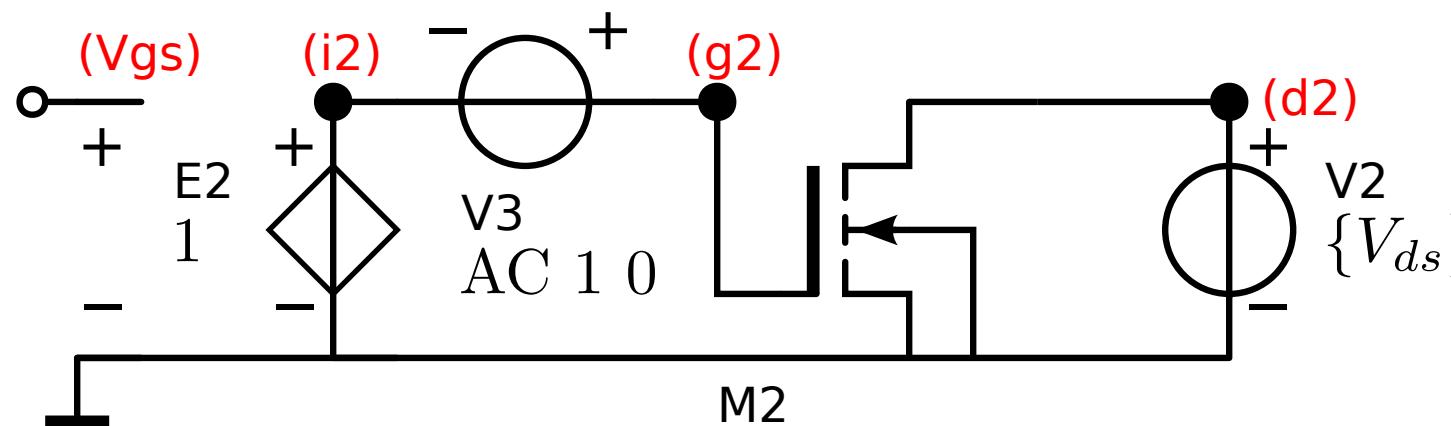
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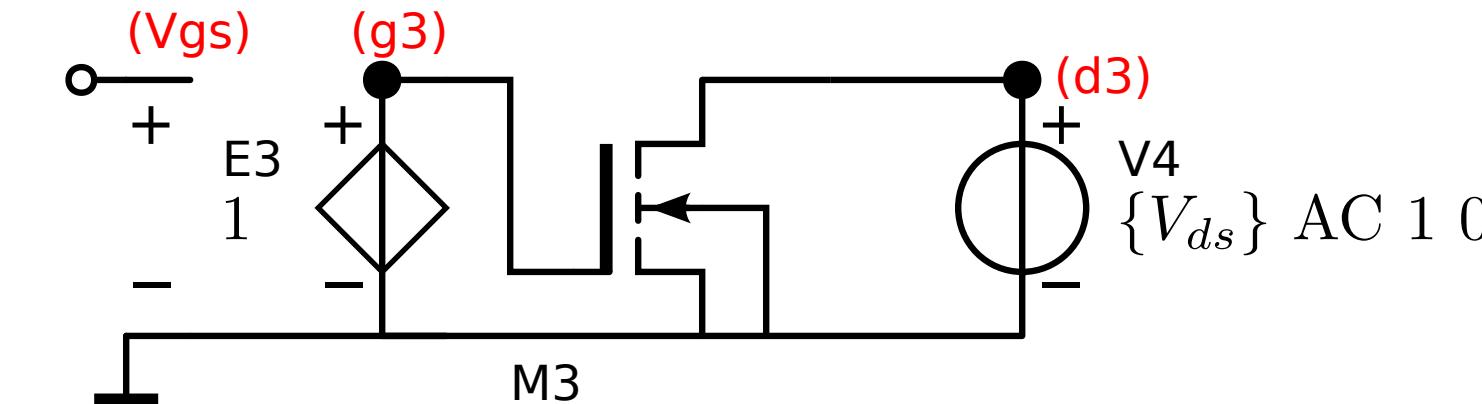
$$g_o = \operatorname{Re} [Y_{2,2}]$$

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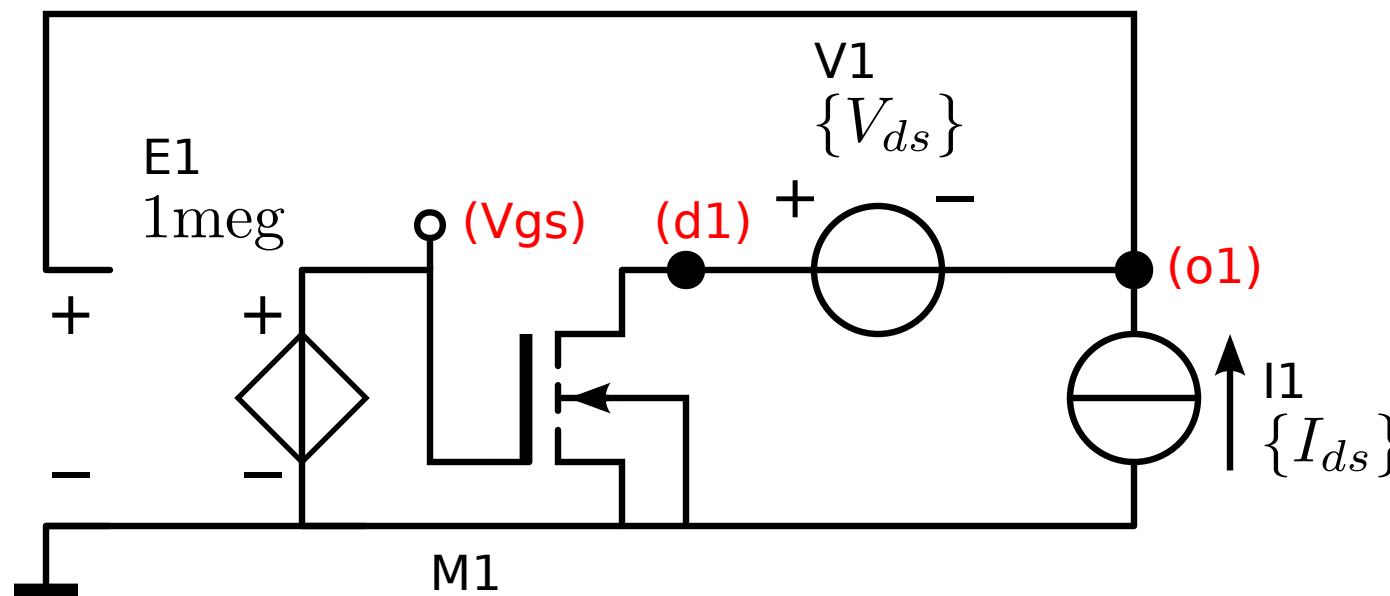


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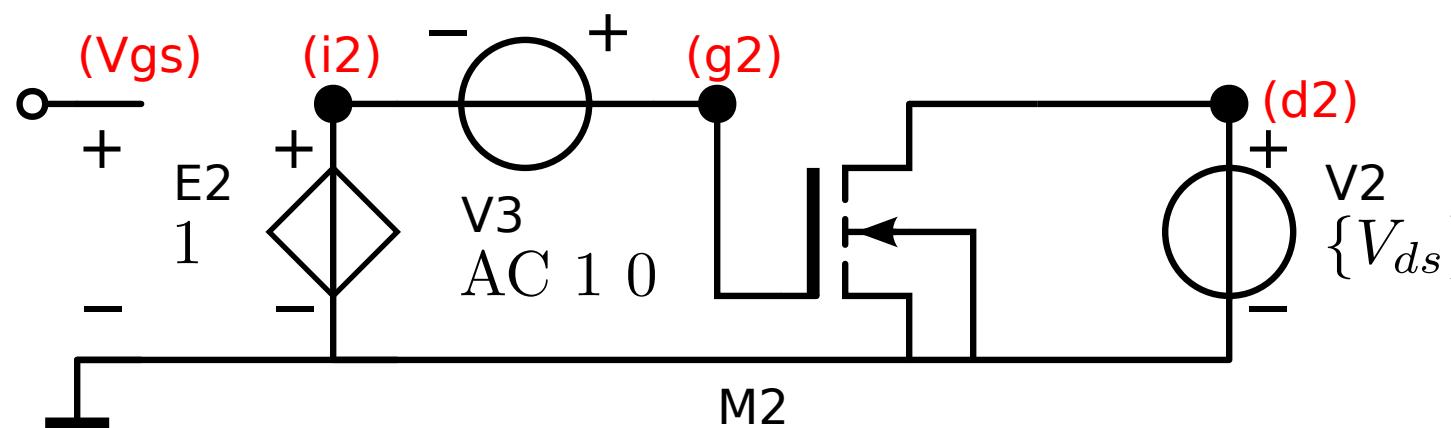
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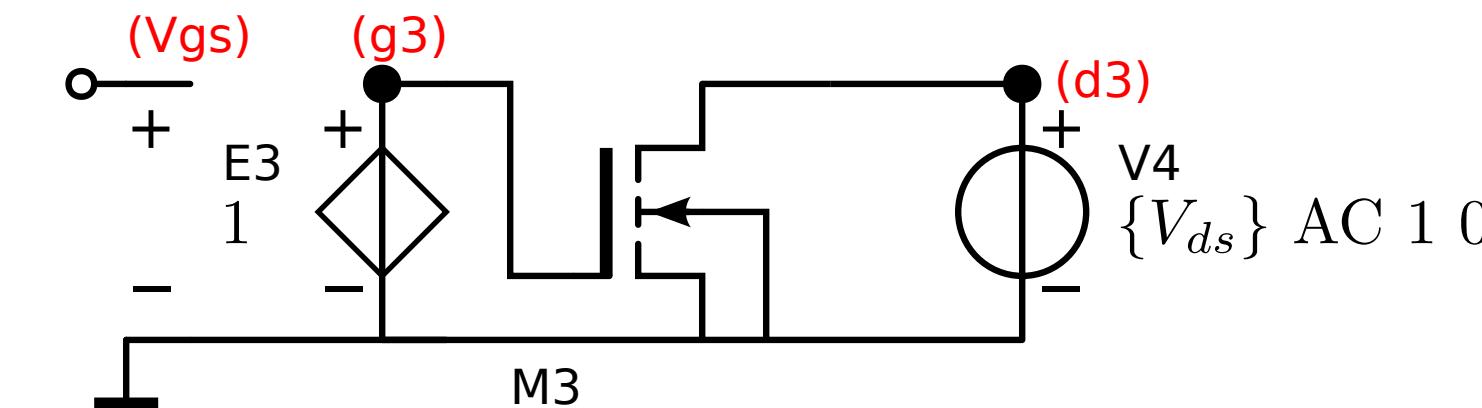
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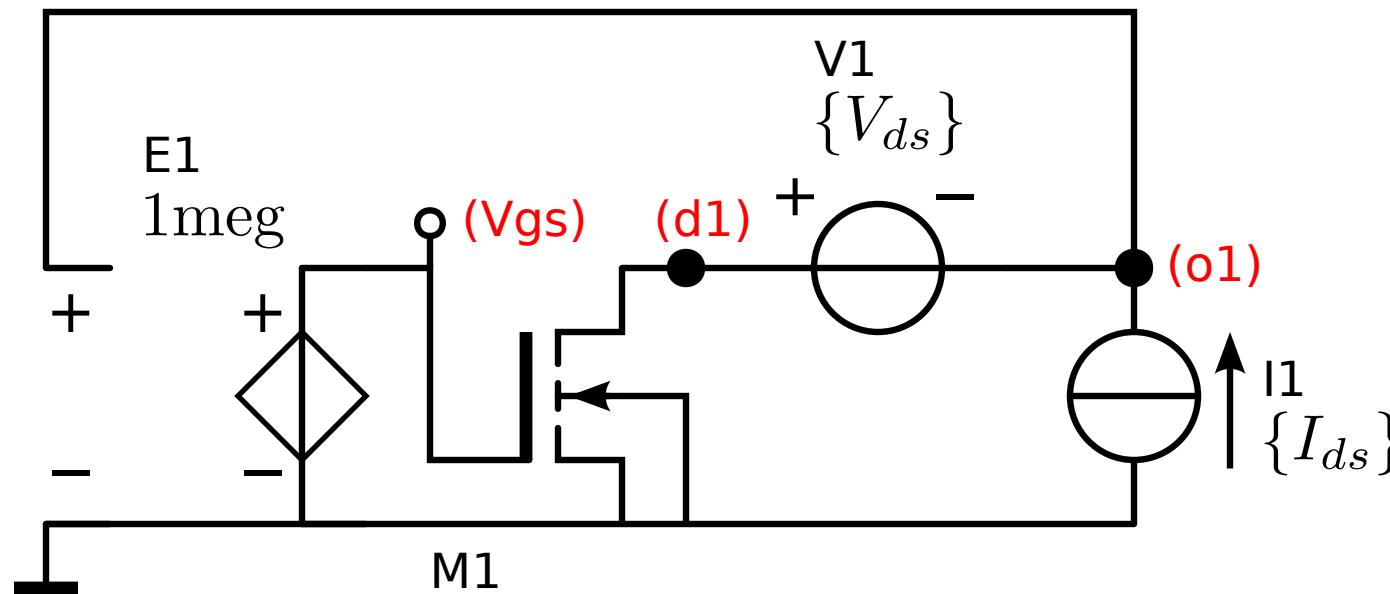


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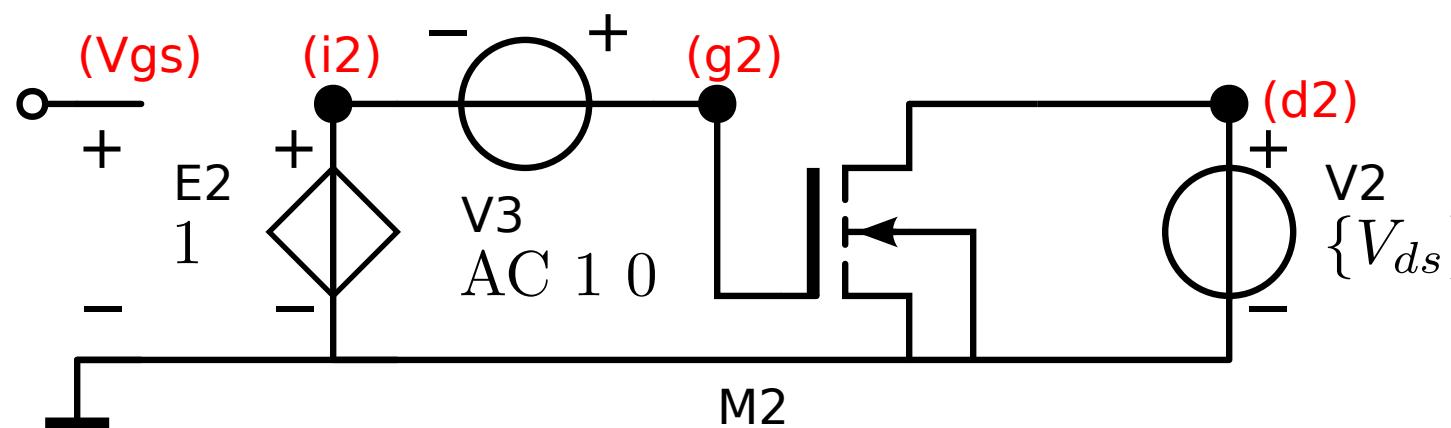
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