Structured Electronic Design

EE4109
Course information

Anton J.M. Montagne

Objective, method and means

Objective

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To master the transistor-level design of application-specific amplifiers

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Method

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Presentation and explanation of a structured design method

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Presentation and explanation of a structured design method
Stepwise application of this theory in the design of an active antenna

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Examples of isolated and combined design problems

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Tools

Symbolic circuit analysis

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Symbolic **L**inear **C**ircuit **A**nalysis **P**rogram

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Staff

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Ir.Dr. C.J.M. (Chris) Verhoeven

Associate Professor
TU Delft Robotics Institute / Theme leader Swarm Robots/ Deputy Director Education
TU Delft Space Institute / Theme leader Space Robotics

Staff

Ir.Dr. C.J.M. (Chris) Verhoeven

Associate Professor
TU Delft Robotics Institute / Theme leader Swarm Robots/ Deputy Director Education
TU Delft Space Institute / Theme leader Space Robotics

Ir. A.J.M. (Anton) Montagne

Visiting Professor
Free-lance consultant analog electronics

Staff

Ir.Dr. C.J.M. (Chris) Verhoeven

Associate Professor
TU Delft Robotics Institute / Theme leader Swarm Robots/ Deputy Director Education
TU Delft Space Institute / Theme leader Space Robotics

Ir. A.J.M. (Anton) Montagne

Visiting Professor
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Contact:

Marion de Vlieger: 015-2786180

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Actively participate during the lectures

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EE3C11: see homologation program

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Assessment

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