## **Structured Electronic Design**

Reduction of biasing errors

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Concept with integrating controller



### Implementation with integrator



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Low offset voltage Low bias current Low offset current



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# SLiCAP negative feedback biasing

# Feedback biasing

## The DC voltage $V_{outDC}$ is:

 $V_{\rm outDC} = 1.0 V_{\rm ref}$ 

The voltage transfer  $A_v$  from source to load is:



For high frequencies, this transfer can be written as:

$$A_v = 20.0$$



(2)

 $(\mathbf{3})$